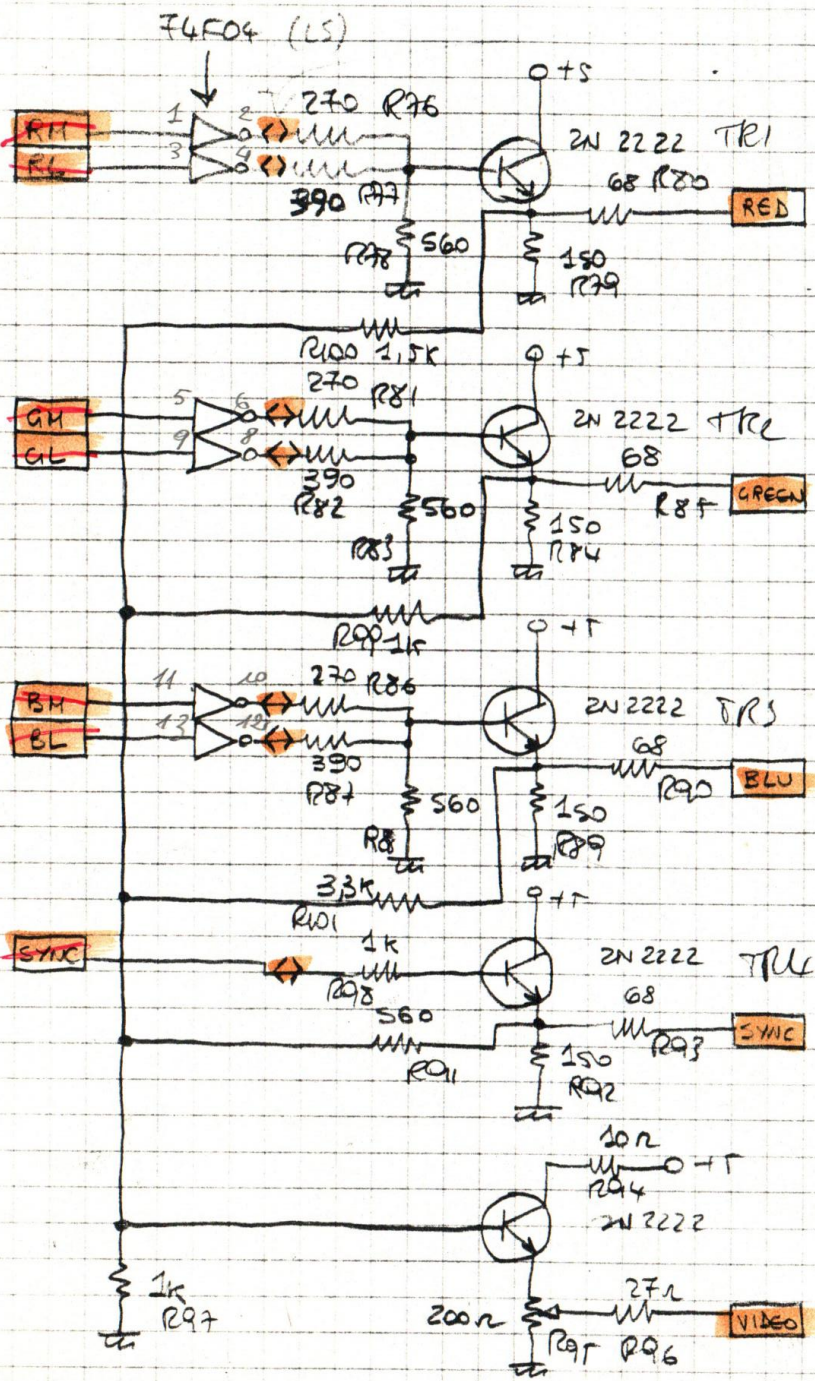
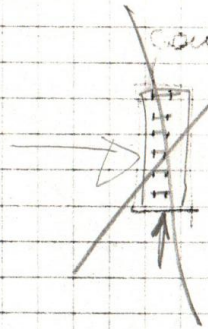
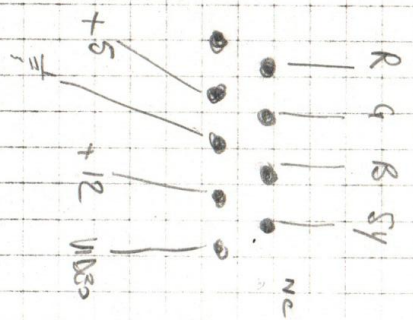
Analog Video Section VMS - Questo circuito va inserito nella scheda analogica esterna -



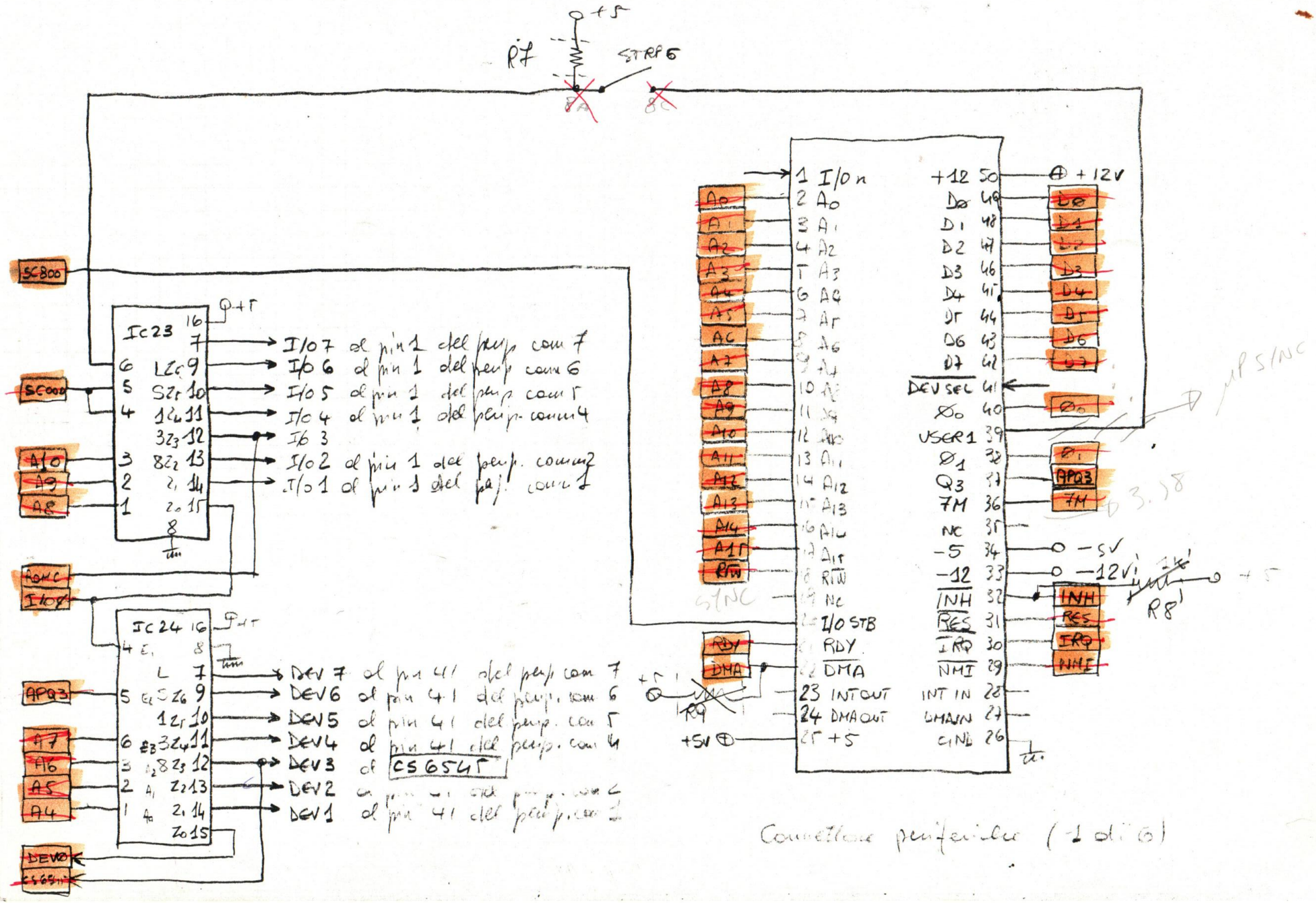


connettore per collegare la scheda grafica -



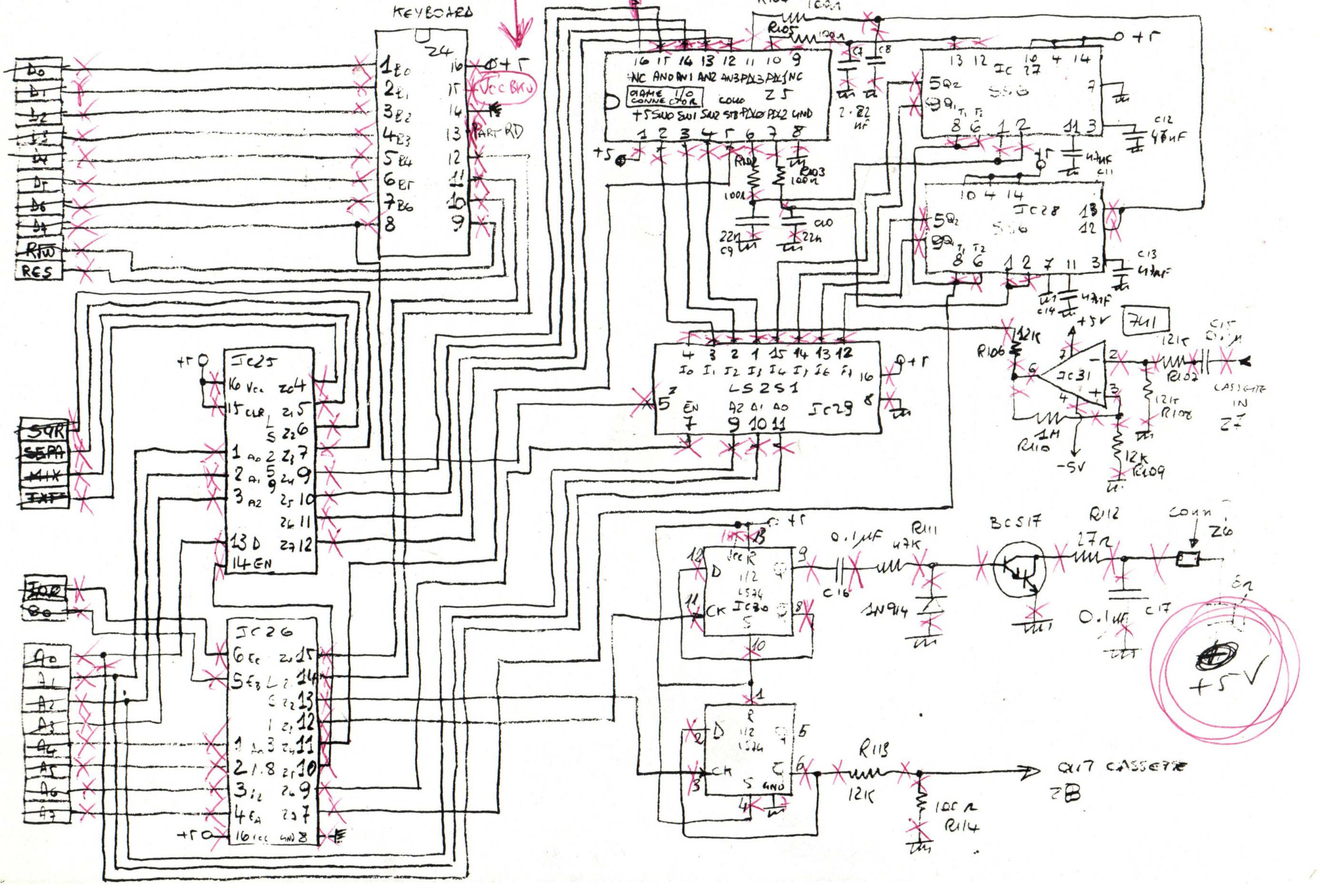
CONNETTORE COLORE

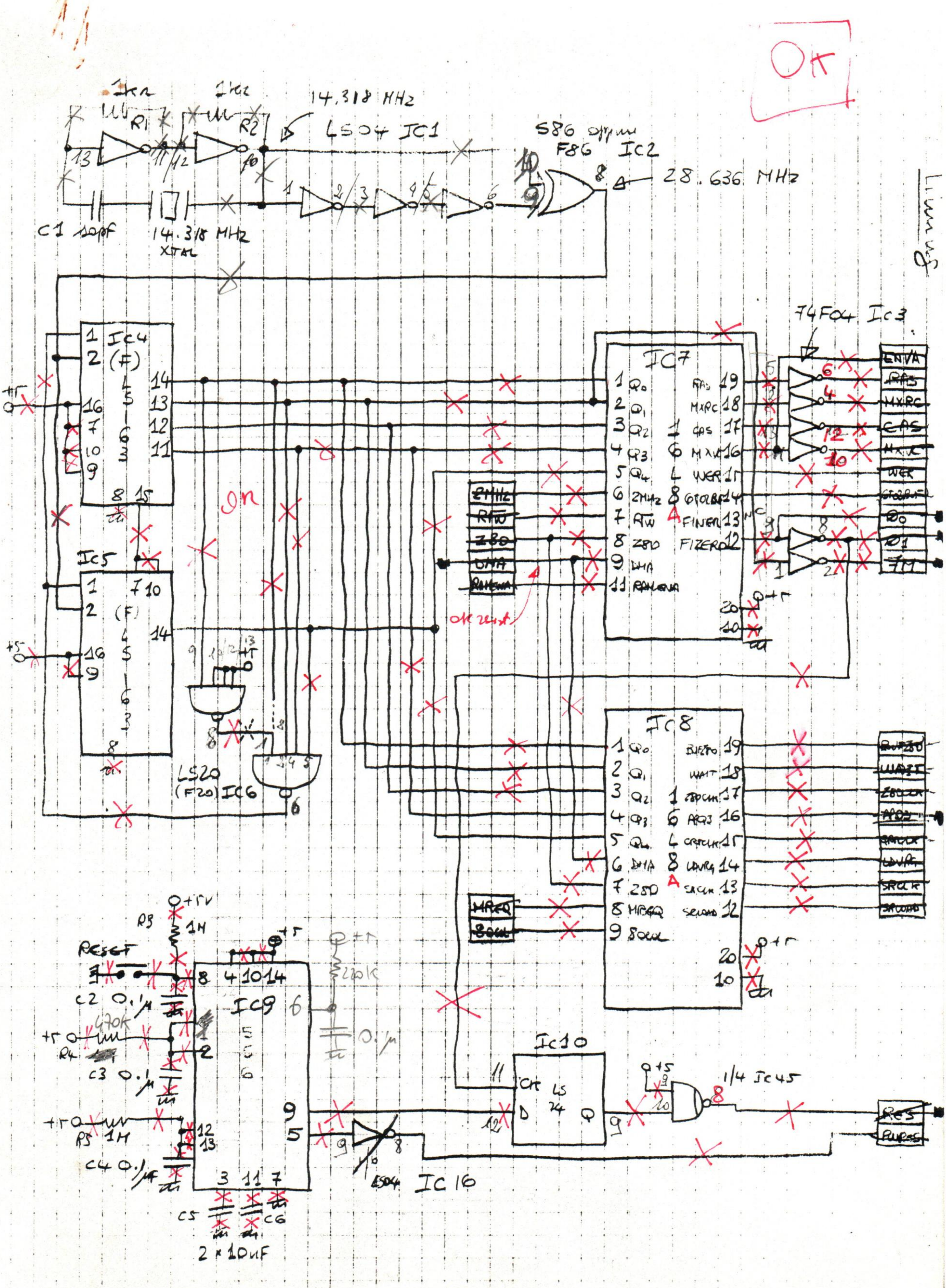
Slots (6) Questa uscita va alloggiata all'esterno dello slot di Core



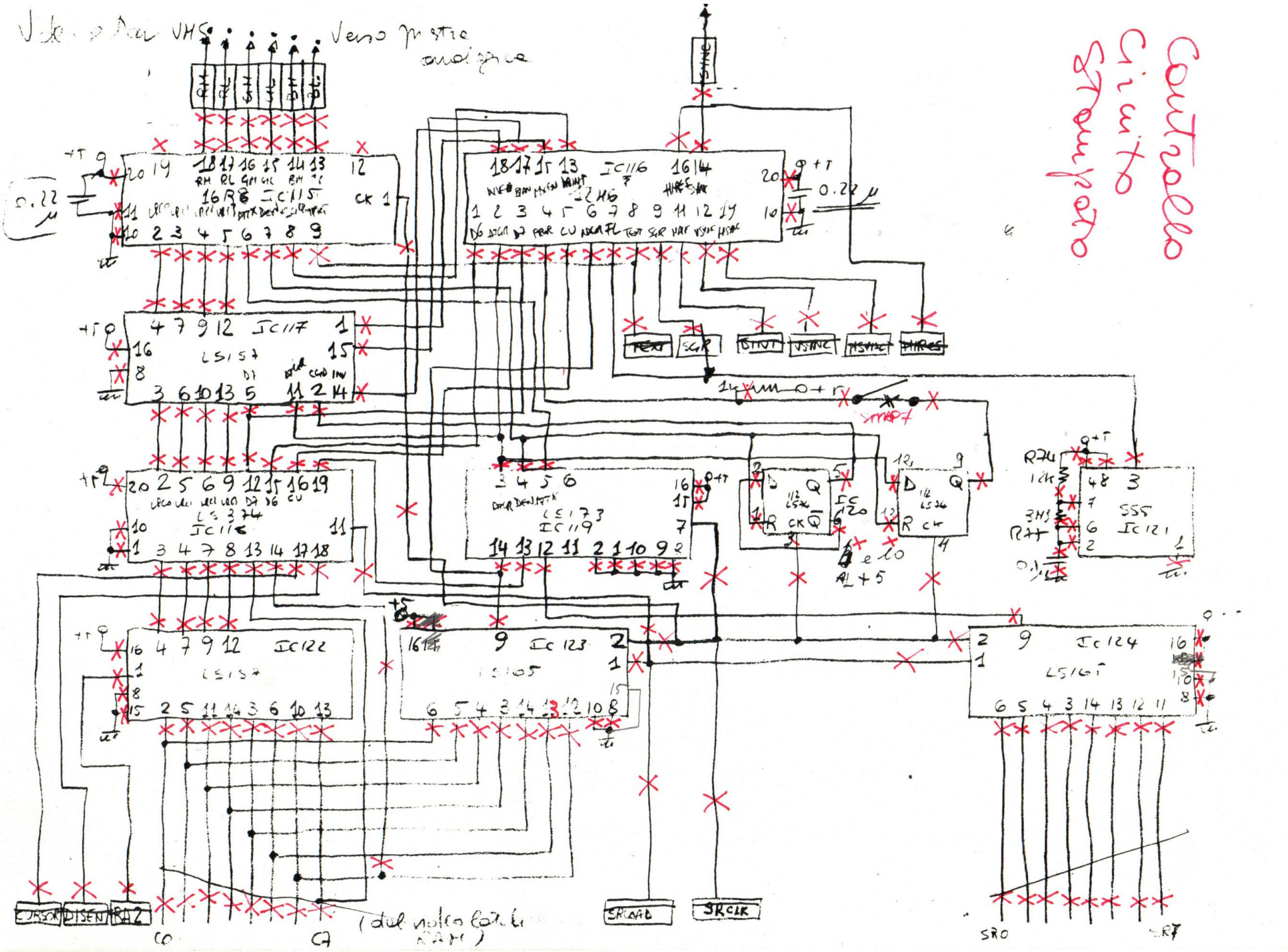
Connettore periferiche (1 di 6)

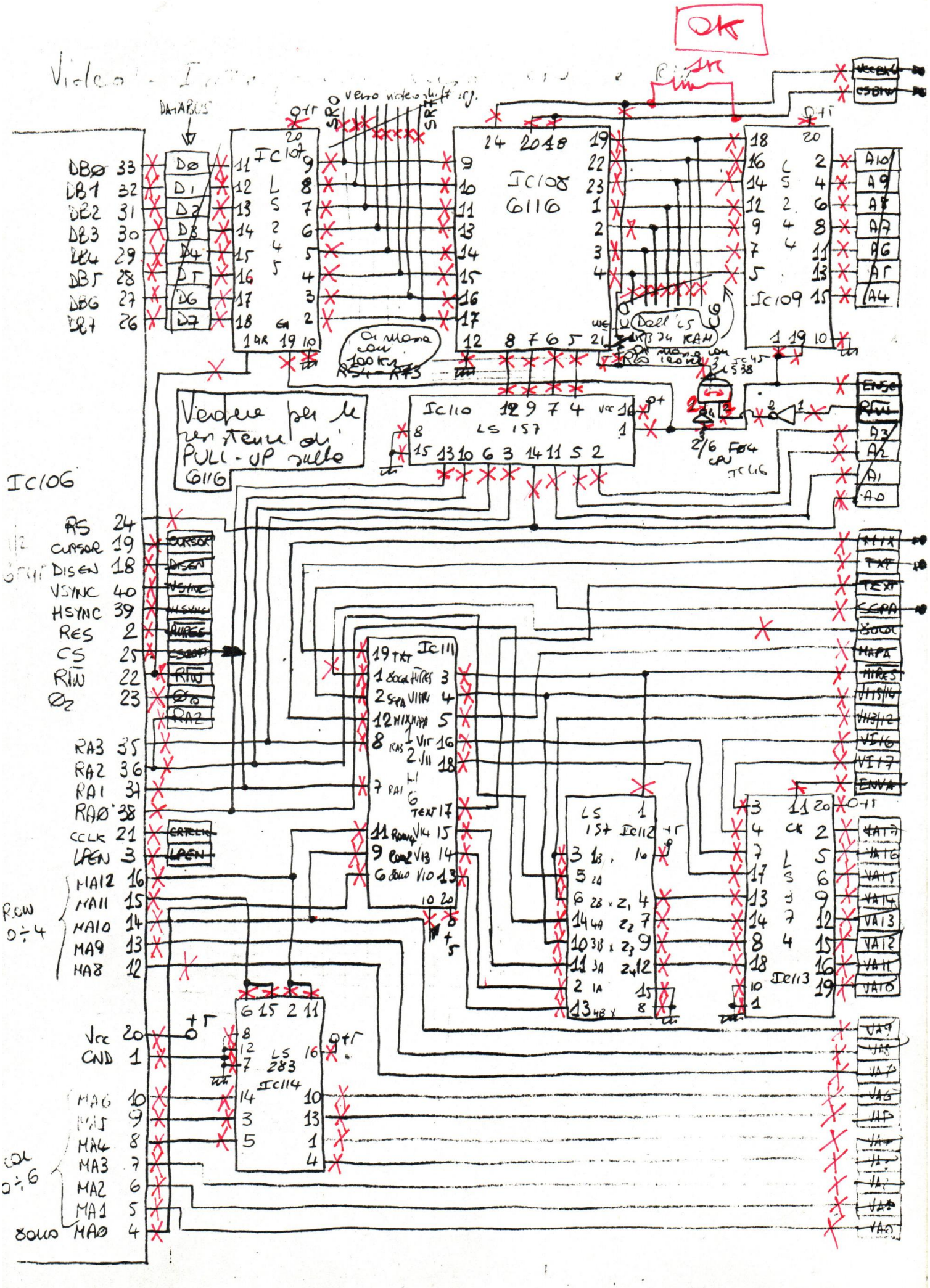
Periferiche interne - Questo circuito è ... alloggiato all'esterno ... OK

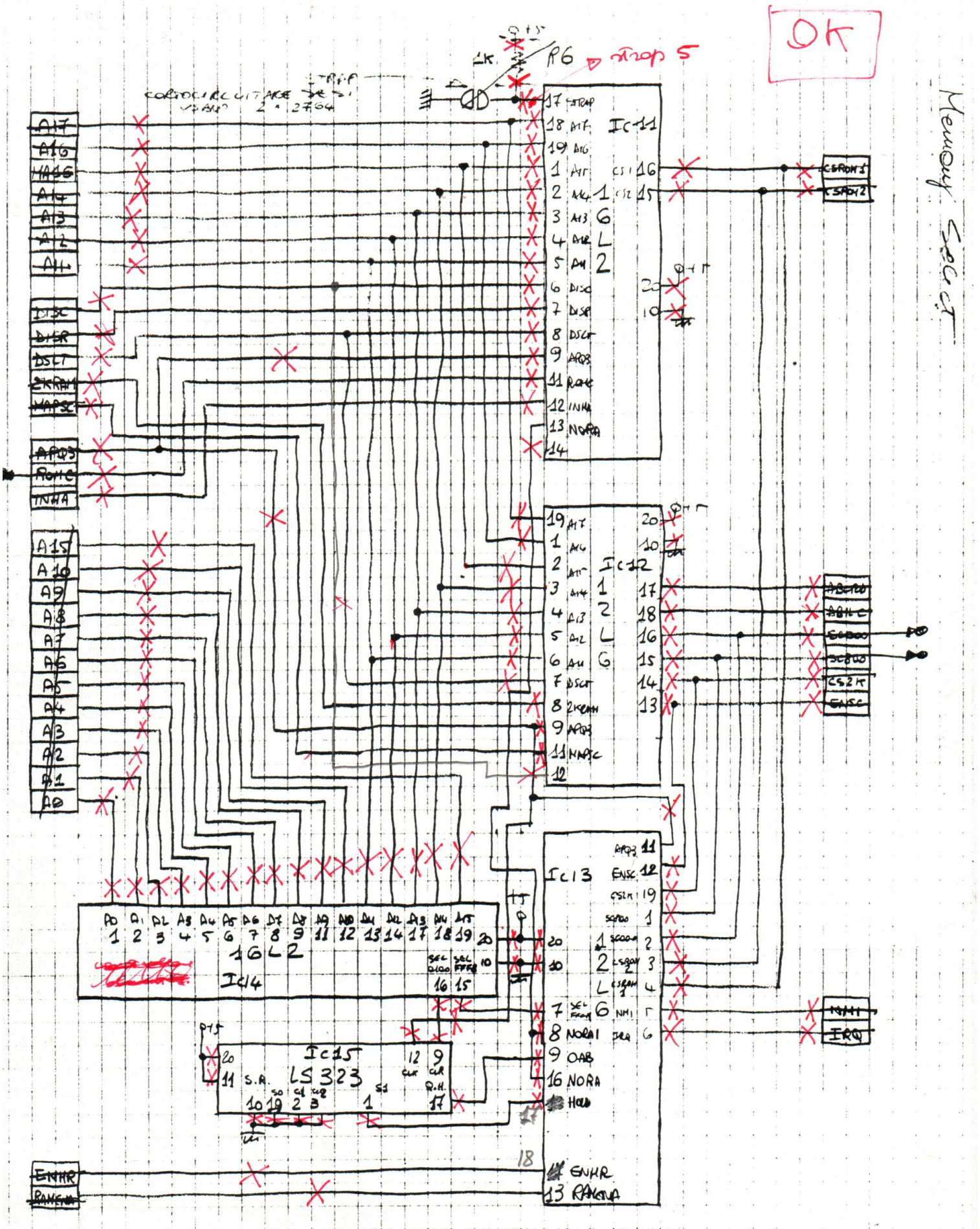




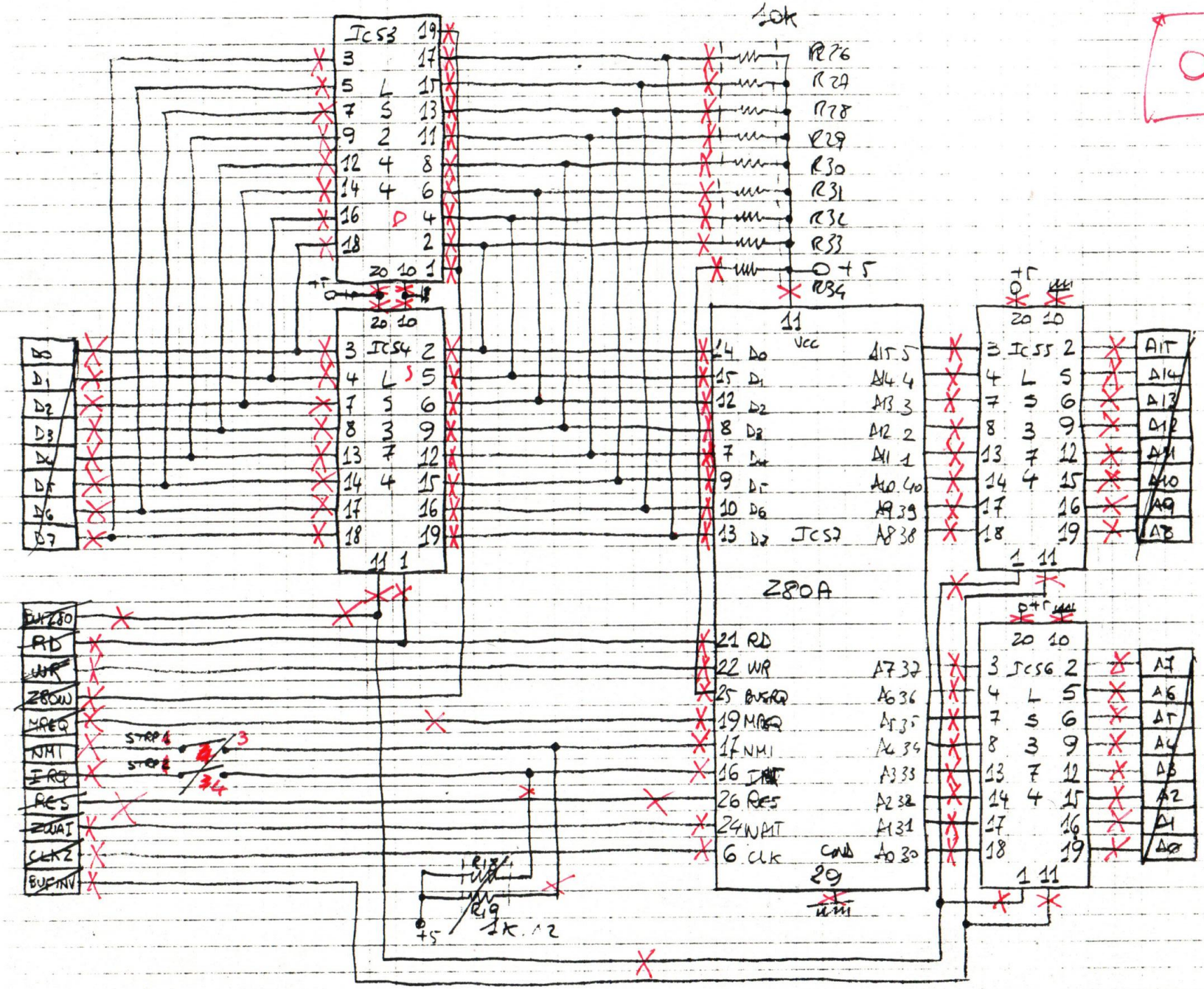
OK
14,318 MHz
LS04 IC1
1kΩ R1
1kΩ R2
C1 10pF
14.318 MHz XTAL
S86
F86 IC2
28.636 MHz
IC4 (F)
IC5 (F)
IC7
74F04 IC3
IC8
LS20 (F20) IC6
ok
ok zero
RESET
R3 1M
C2 0,1µ
470k
R4
C3 0,1µ
R5 1M
C4 0,1µF
IC9
C5
C6
2 × 10uF
220k
0,1µ
LS04 IC16
IC10
LS 74
1/4 IC45


Controllo
Circuito
Stampato
Verso matrice
audizione
SYNC
TEXT
SCLR
BTINT
VSINC
HSYNC
HIRES
SRLOAD
SRCLK
CURSOR
DISEN
SRO

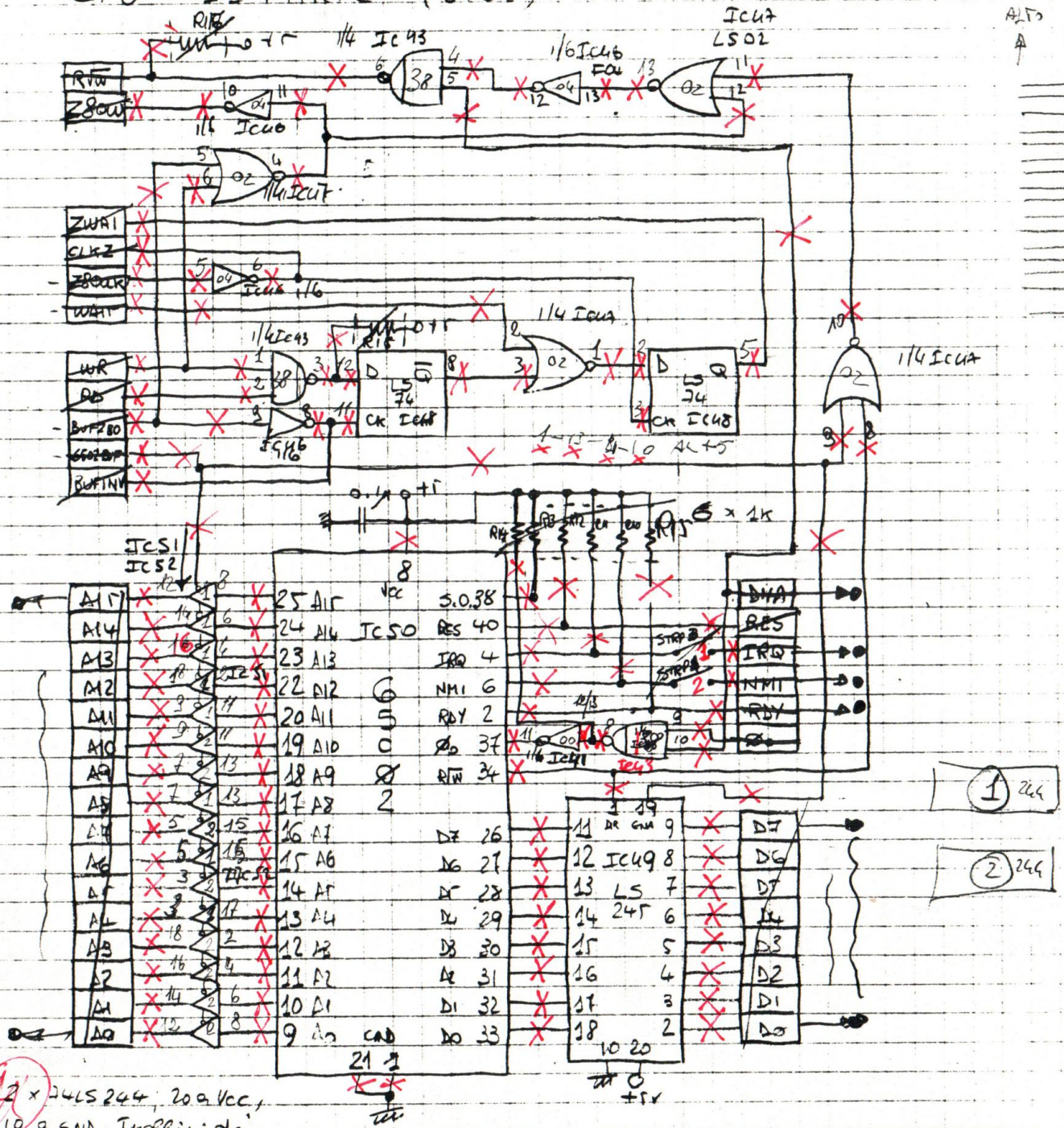

OK
DATABUS
IC106
IC108
6116
IC109
IC110
LS 157
IC111
IC112
IC113
IC114
LS 283
Vedere per le resistenze di PULL-UP sulle 6116
RS
CURSOR
DISEN
VSYNC
HSYNC
RES
CS
R/W
Φ2
RA3
RA2
RA1
RA0
CCLK
LPEN
MA12
MA11
MA10
MA9
MA8
MA7
MA6
MA5
MA4
MA3
MA2
MA1
MA0
Vcc
GND
ROW 0÷4
COL 0÷6
DB0
DB1
DB2
DB3
DB4
DB5
DB6
DB7

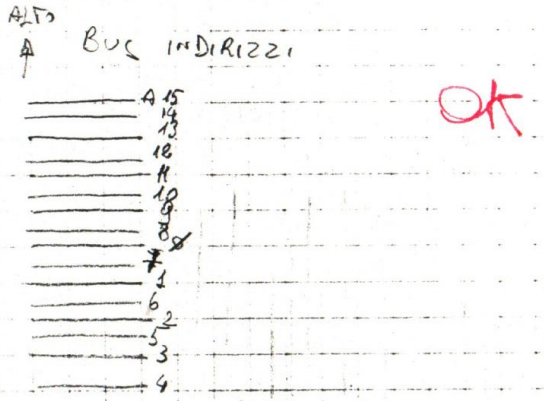

OK
Memory Select
R6
Ic 11
Ic 12
Ic 13
Ic 14
Ic 15
LS 323
A17
A16
A15
A14
A13
A12
A11
A10
A9
A8
A7
A6
A5
A4
A3
A2
A1
A0
CSROM1
CSROM2
IRQ
NMI
ENHR
RAMEN

CPU – 2ª PARTE (Z80)



CPU – 1ª PARTE (6502)





2 x 74LS244, 20 a Vcc, 10 a GND. Ipotizzo di escludere dal bus dati i pin 1 e 19 esterni

ROM

OK

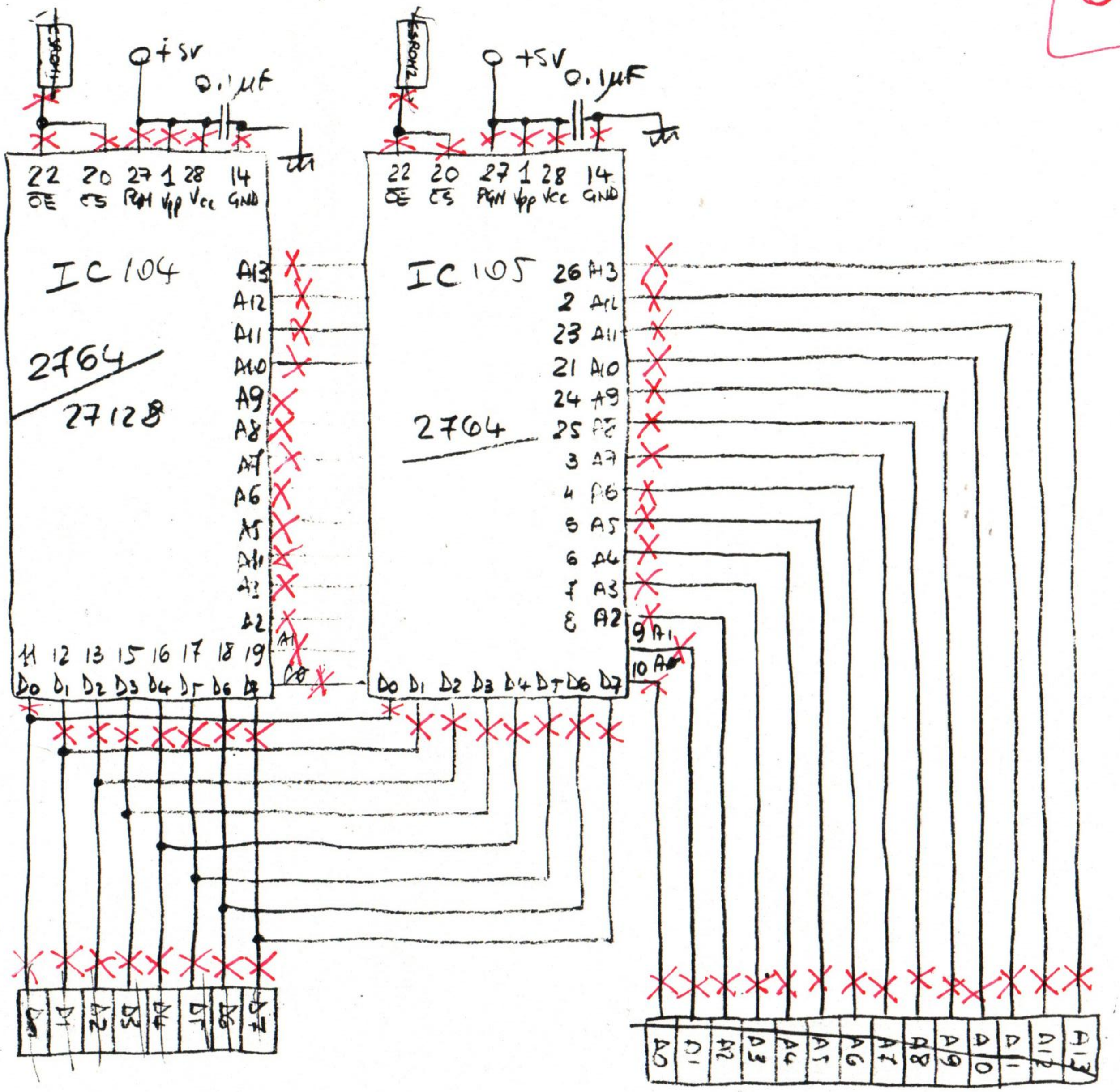
CSROM 1 e 2 sono
di pertinenza alle ?

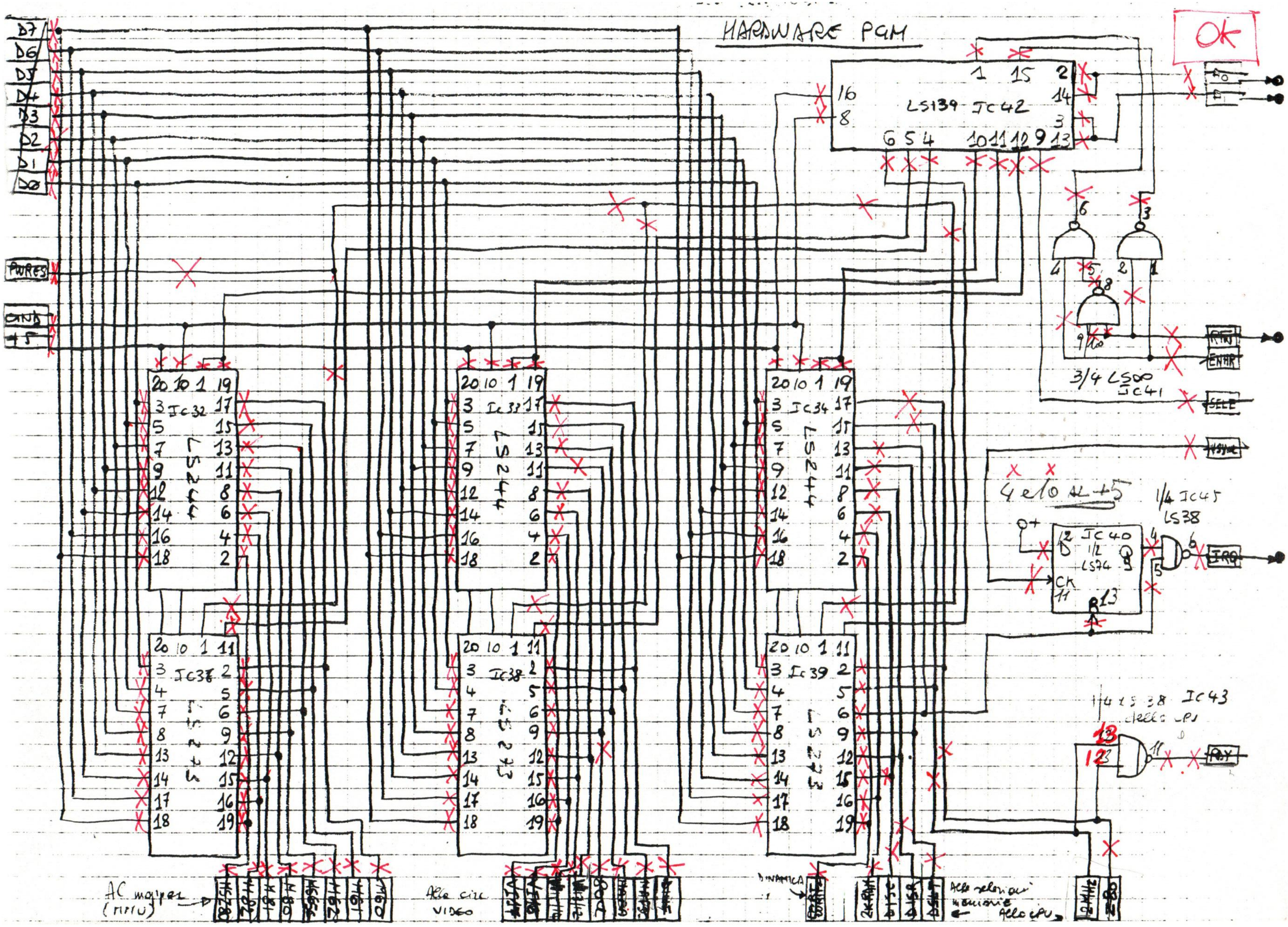
$\emptyset_0$, A17, A16, A15, A14, A13
INHA, ENAC, [illegible], DI SR, DSLT

le stesse poi può generare
anche enable slots e
enable DYN RAM ?

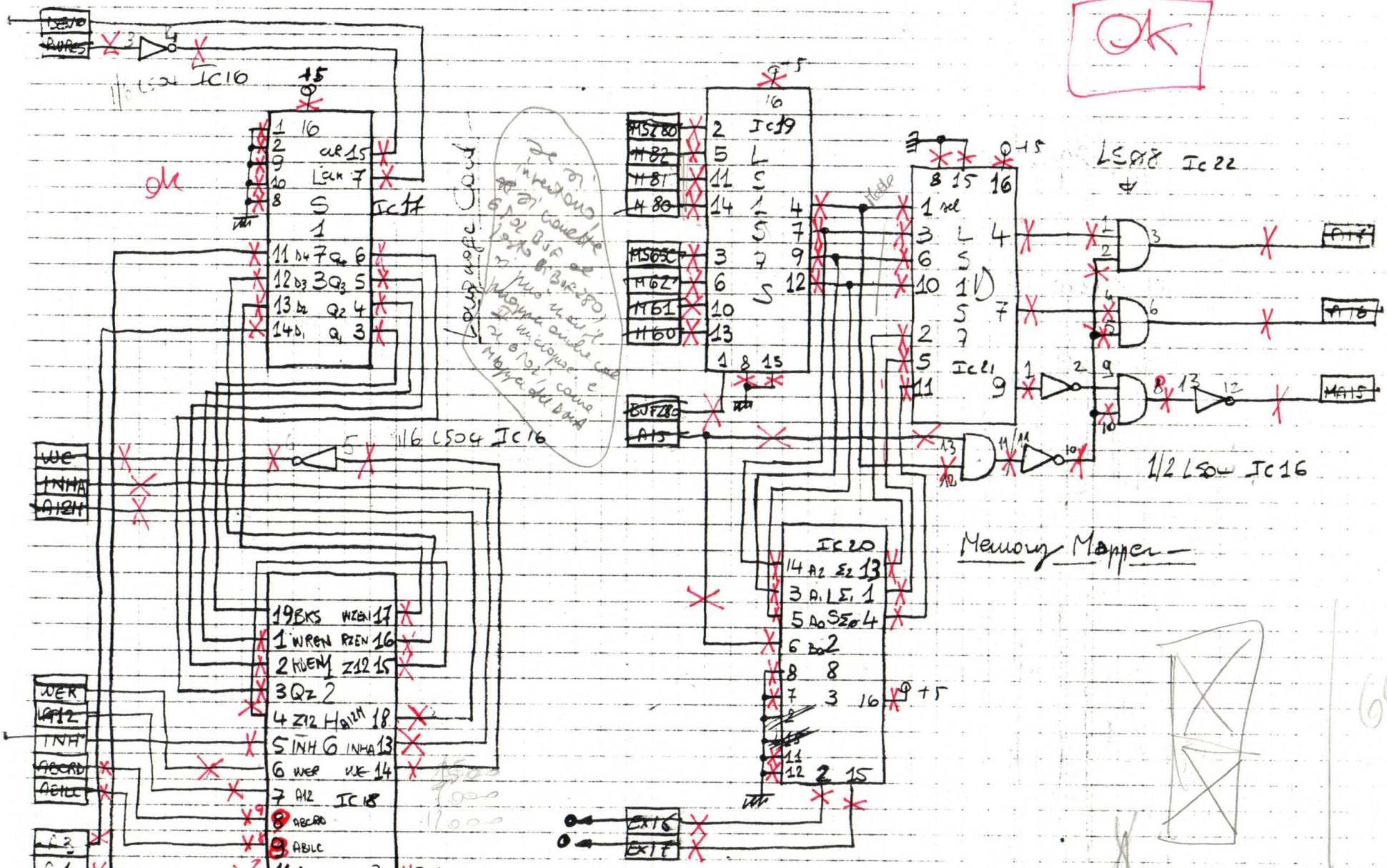
Segnali supplementari richiesti:

2KRAM - MAPSC, potrebbe essere
anche l'OR dei 2.

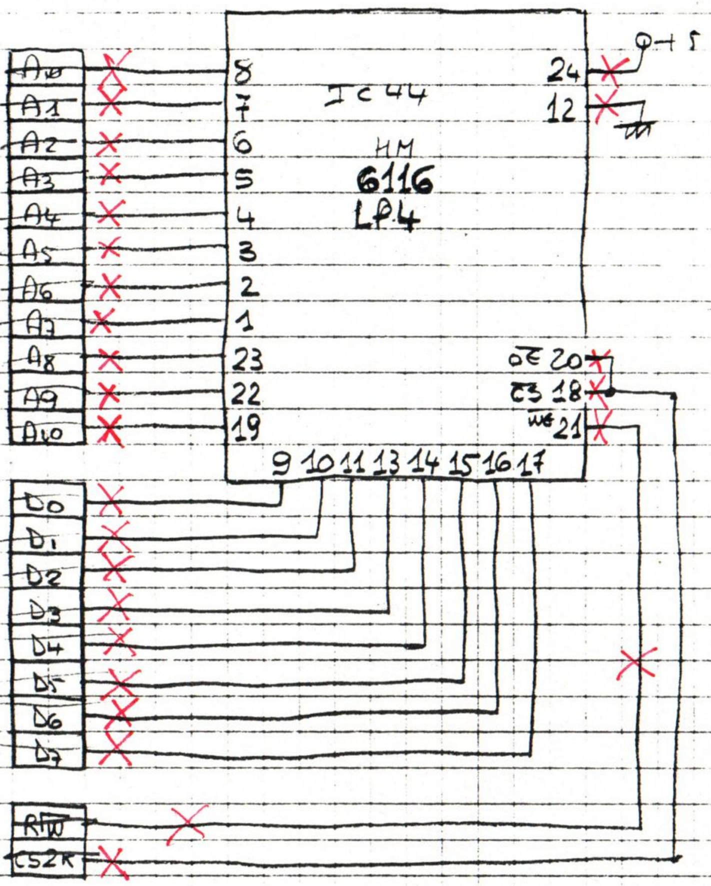



HARDWARE PCM
OK
LS139 IC42
3/4 LS00 IC41
4 e 10 AL +5
1/4 IC45 LS38
IC40 1/2 LS74
1/4 LS38 IC43
IC32 LS244
IC33 LS244
IC34 LS244
IC37 LS273
IC38 LS273
IC39 LS273
D7
D6
D5
D4
D3
D2
D1
D0
PWRES
GND
+5
RTN
ENAR
SELE
IRQ
RDY
Alle circ VIDEO

M.M.U – Memory Management Unit –



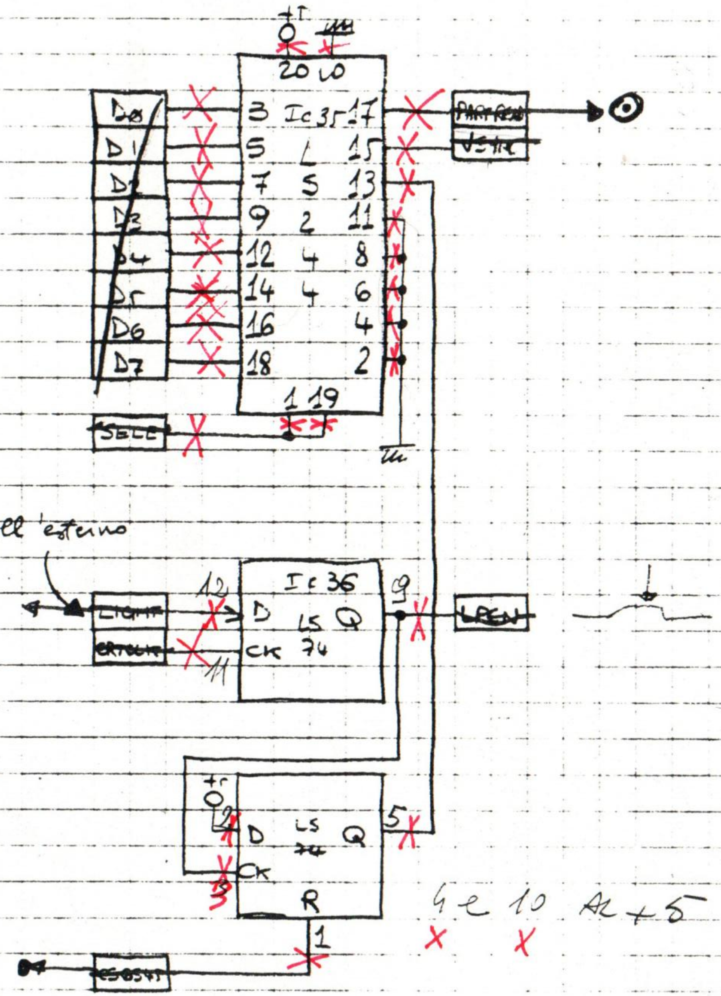
2K RAM -

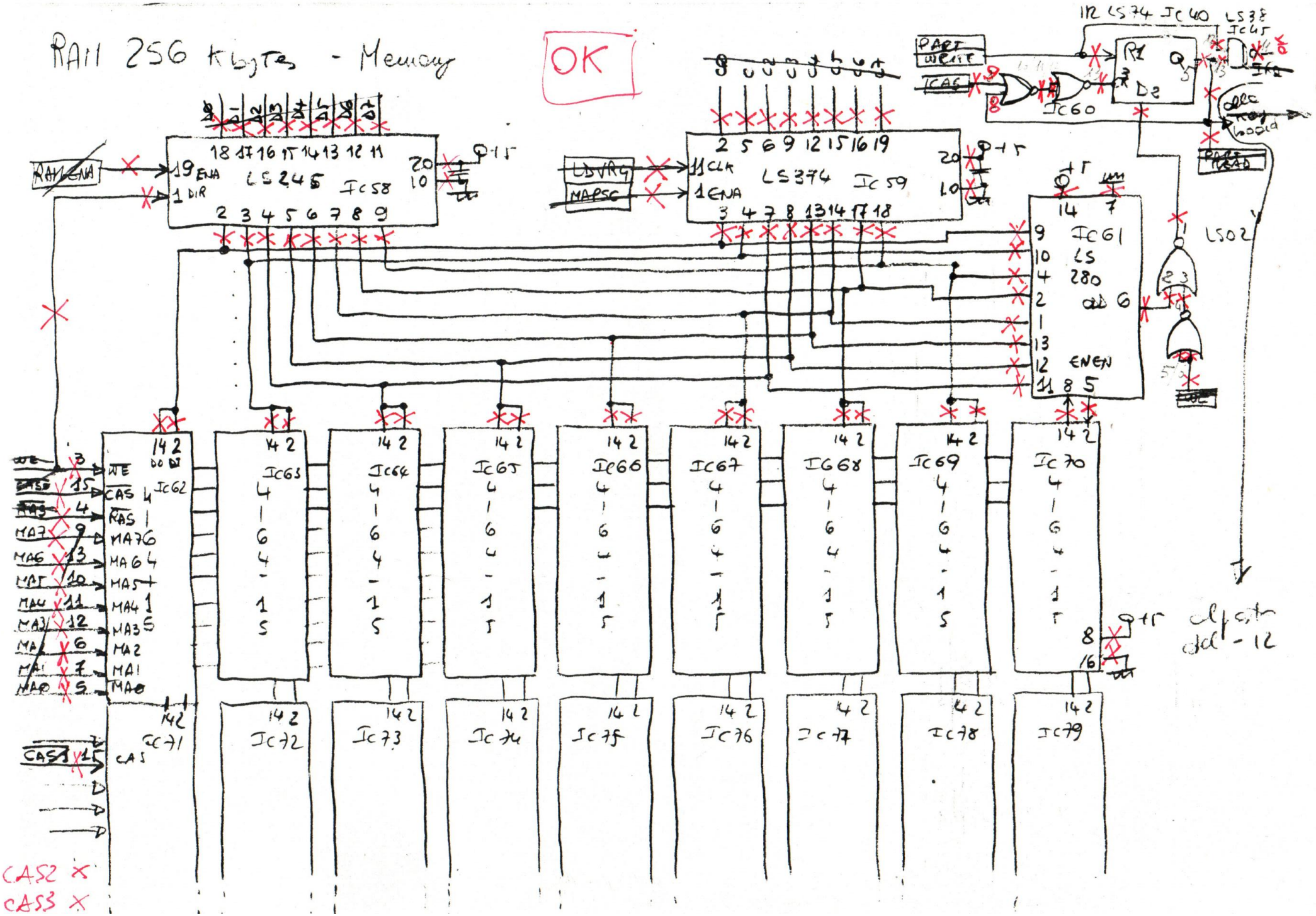


Se non è lo fa in velocità
lasciare solo $\overline{OE}$ comune
e connettere il $\overline{CS}$ fisso a
massa, se non dà problemi
in scrittura (?)

Circuiteria light pen e altro.

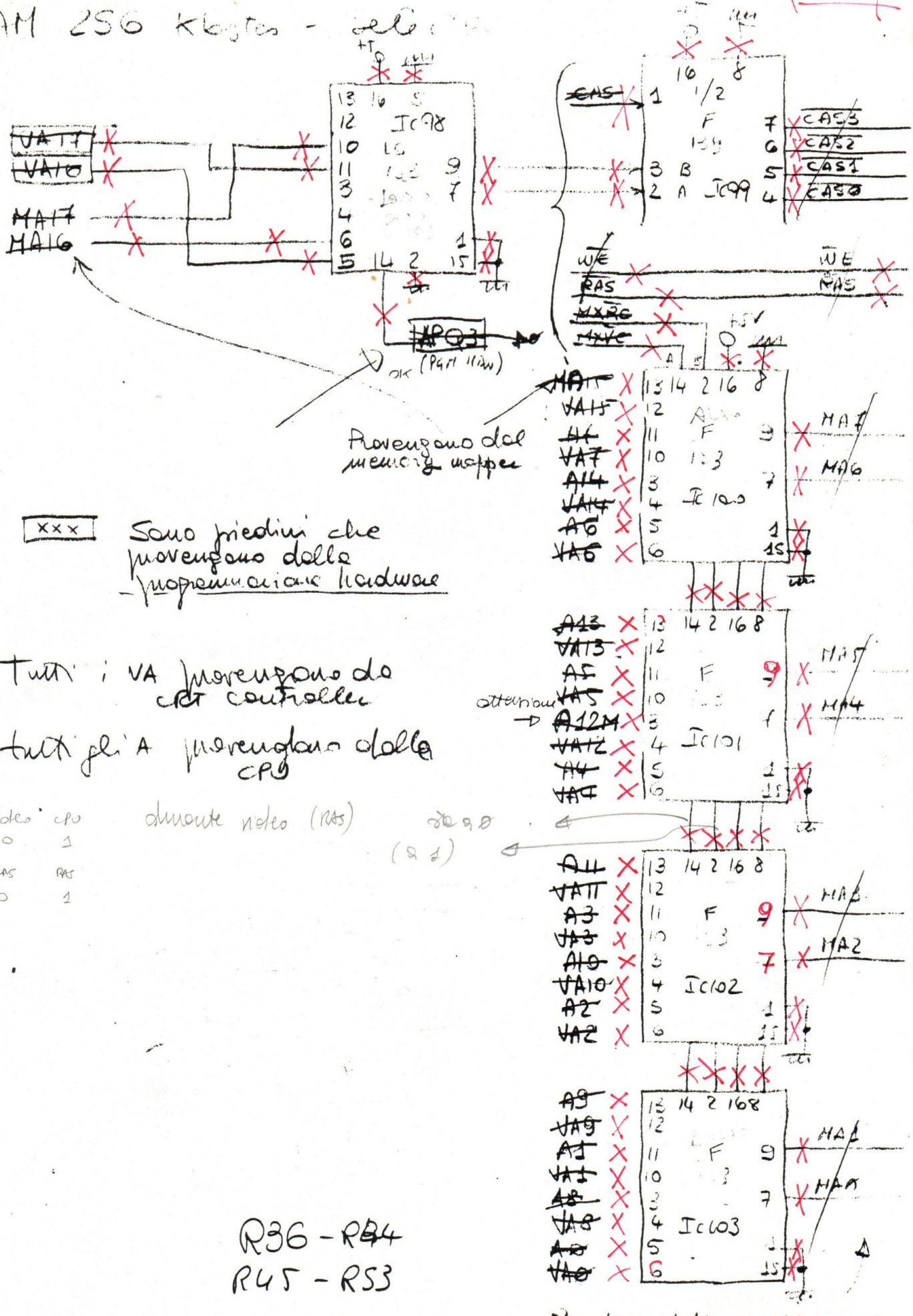
OK




RAM 256 kbytes - Memory
OK
RAMENA
LS245 IC58
LS374 IC59
LDVRQ
MAPSC
IC60
IC61
LS280
LS02
PART WRITE
ICAS
IC62
IC63
IC64
IC65
IC66
IC67
IC68
IC69
IC70
IC71
IC72
IC73
IC74
IC75
IC76
IC77
IC78
IC79
CAS1
CAS2
CAS3

OK

RAM 256 kbytes - el



xxx Sono piedini che provengono dalla programmazione hardware

Tutti i VA provengono da CRT controller

tutti gli A provengono dalla CPU

	video	CPU
14	0	1
	CAS	RAS
2	0	1

durante video (RAS)

R36 - R44
R45 - R53

Resistenze : 1K verso +5 e massa.