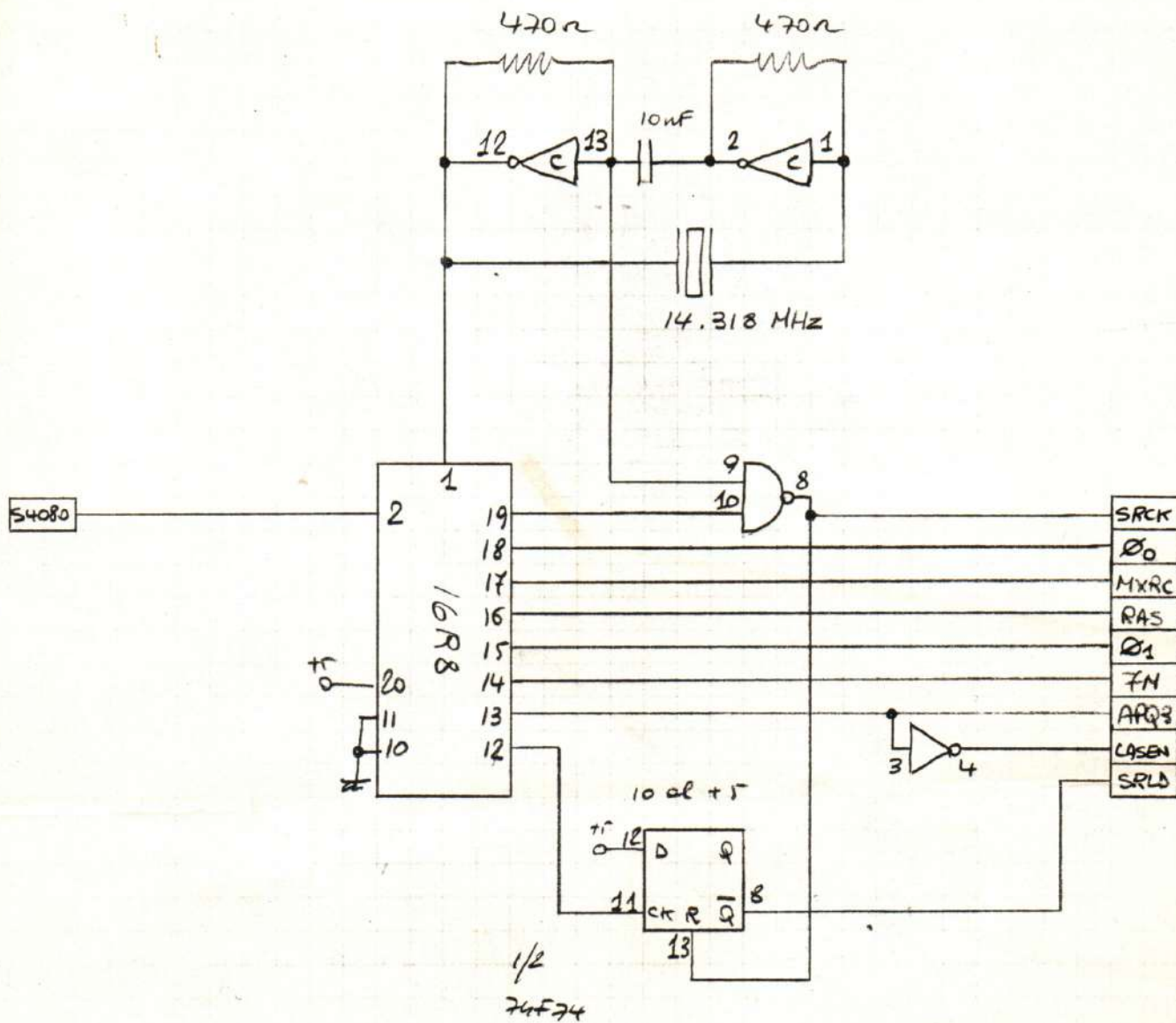
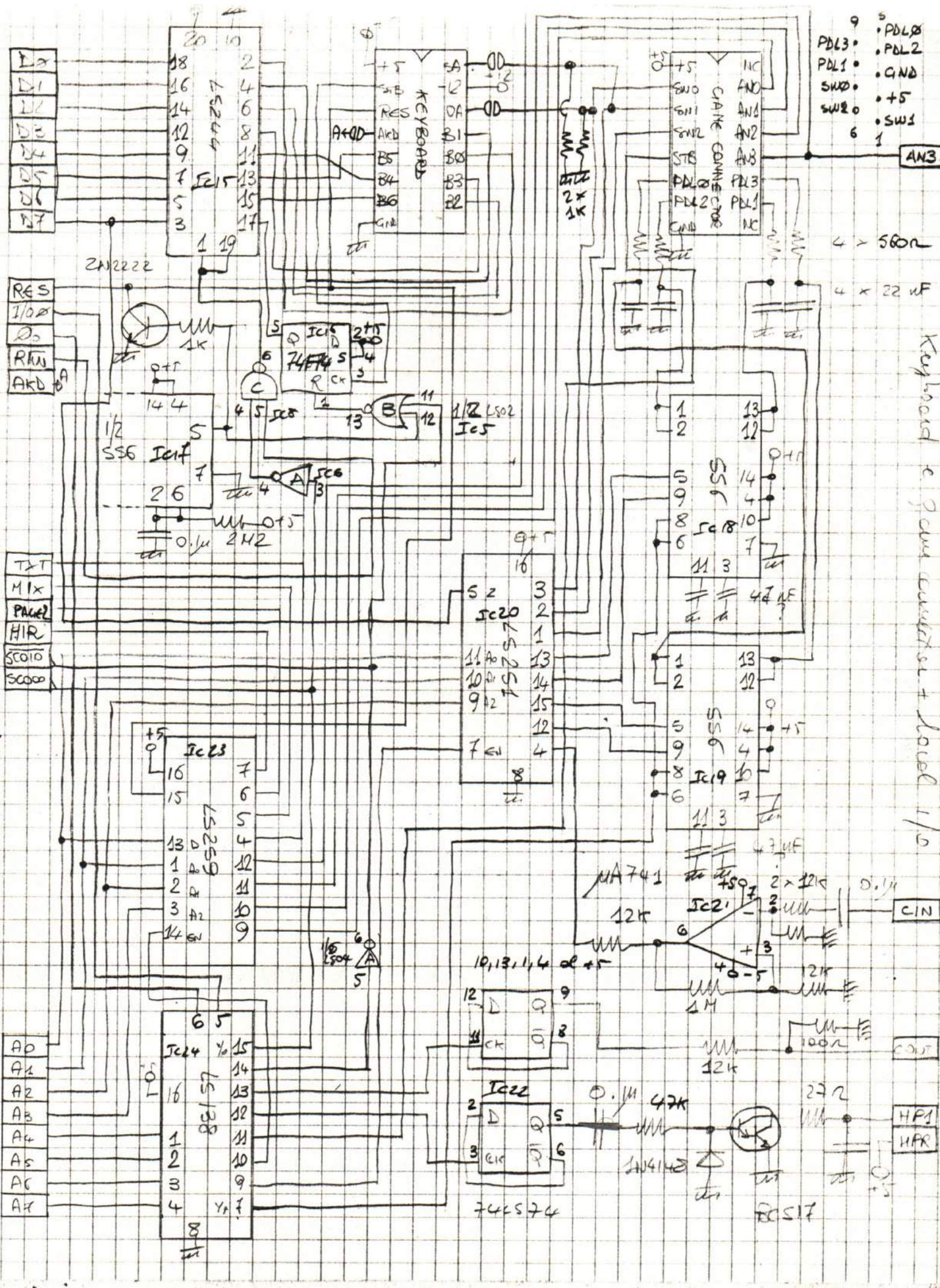


# SEMI COMPUTER

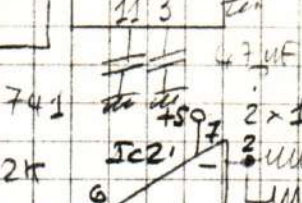
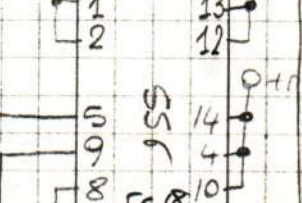
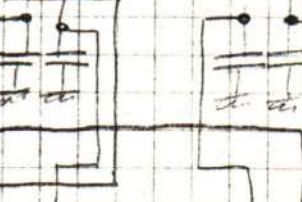
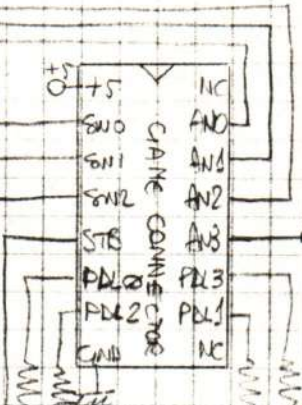
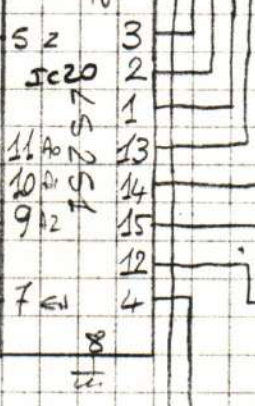
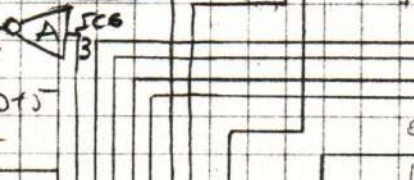
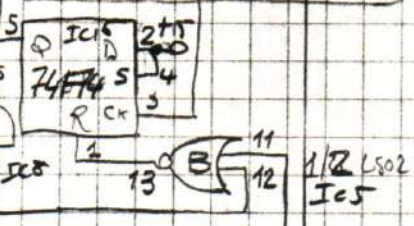
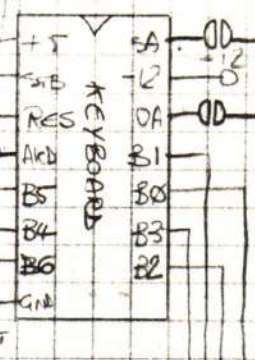
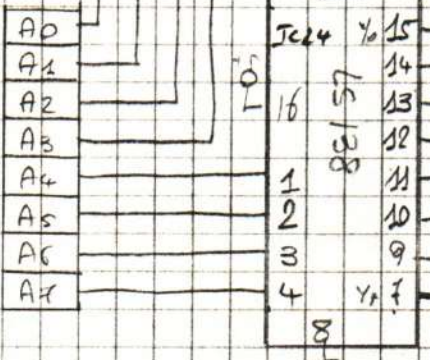
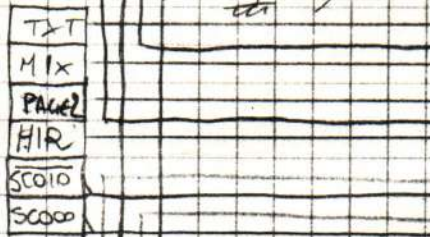
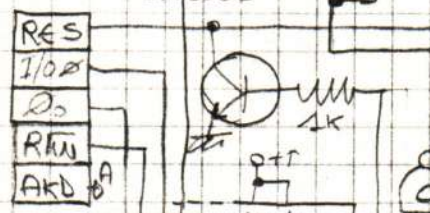
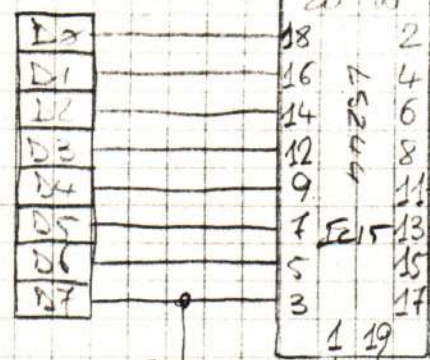
Timing





- 9 • PDL0
- 5 • PDL1
- PDL2
- PDL3
- GND
- SW0 • +5
- SW20 • SW1
- 6 1

Keyboard & I/O controller + level 1/0



4 x 560n

4 x 22uF

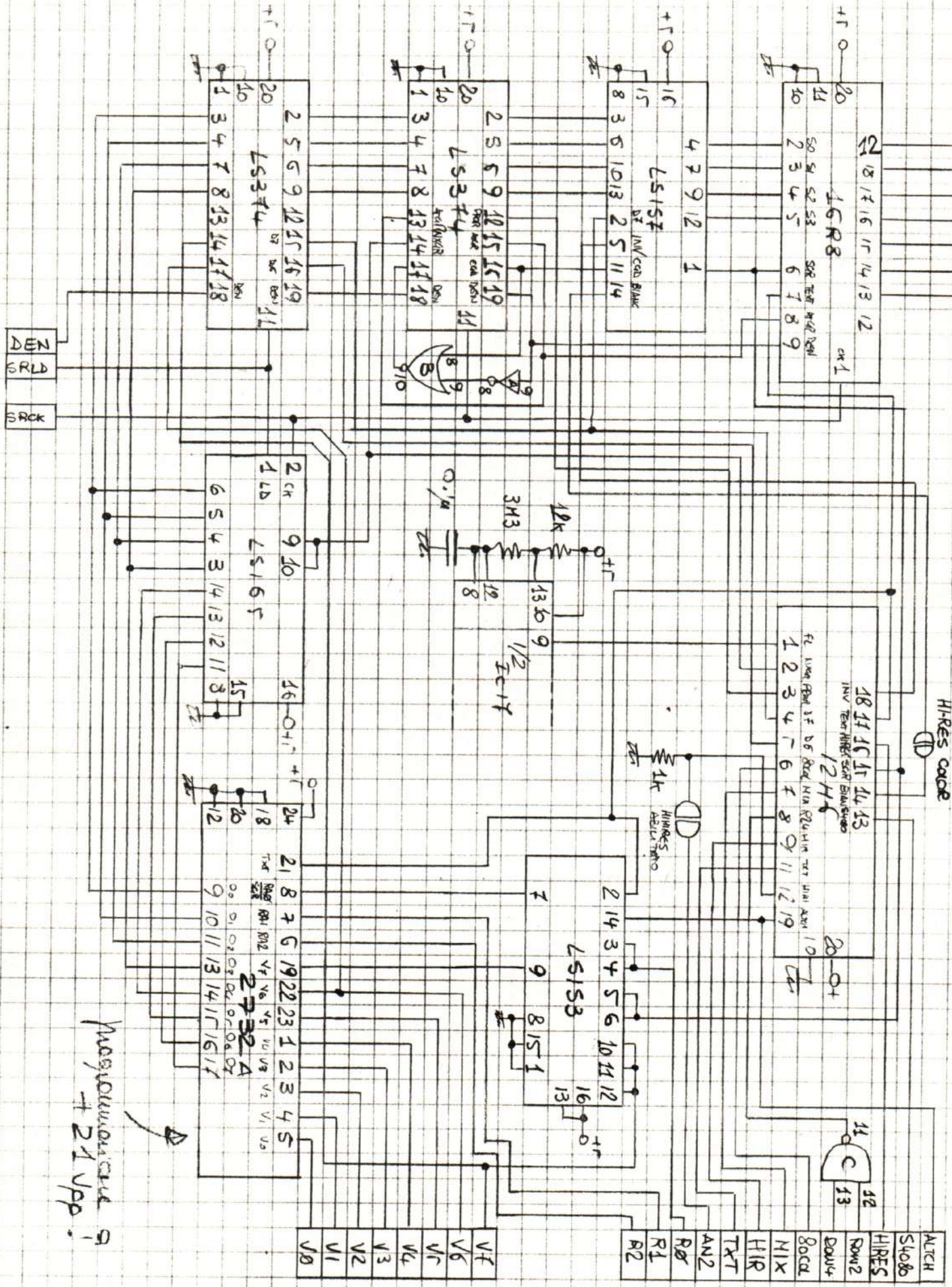
CIN

CONT

HP1 HP2

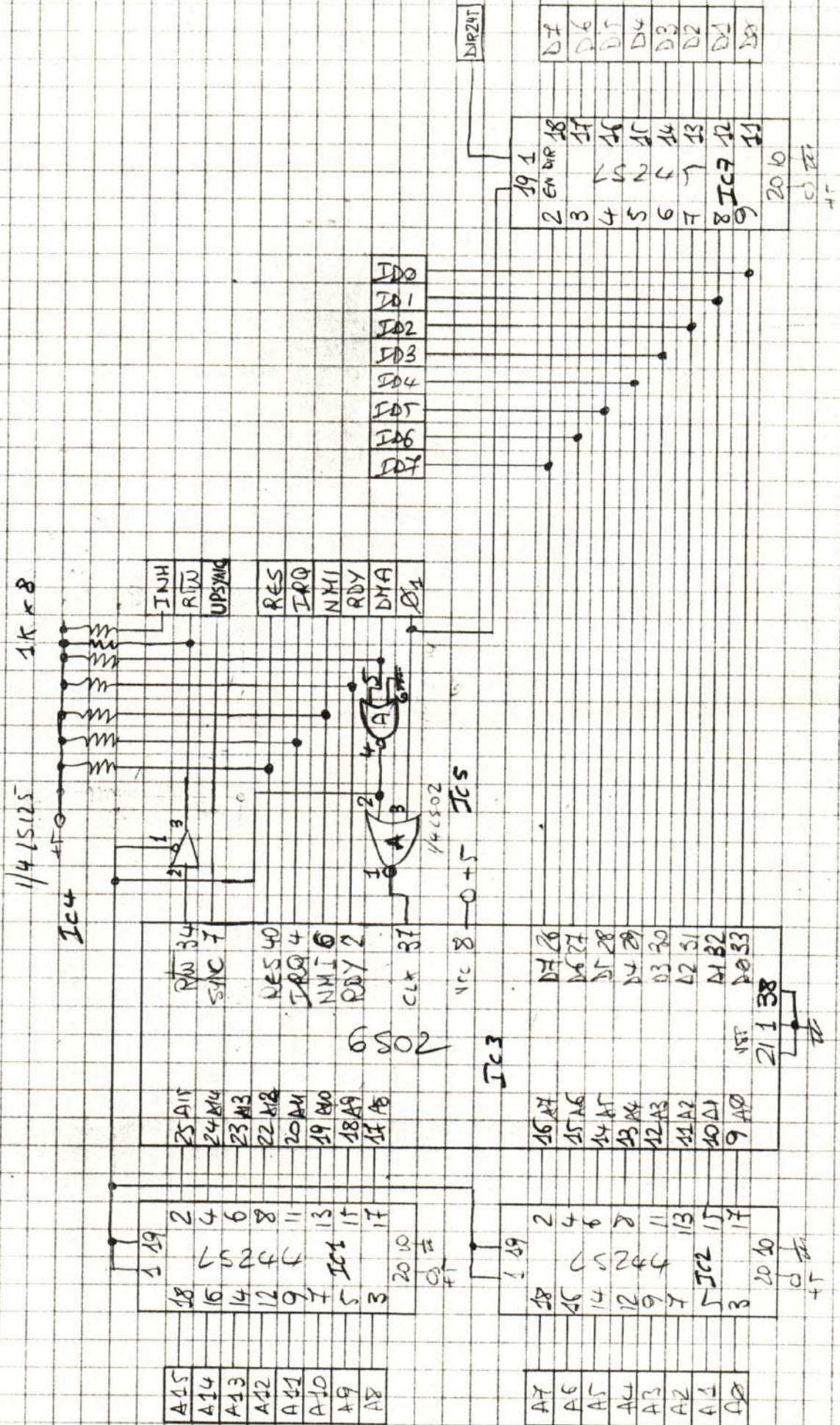
13TMD
R4
R2
H5
H7
H10
H11

V. o. o. o. n. g. u. e. l. p. e. c. t. r. o. n. i. c. s.

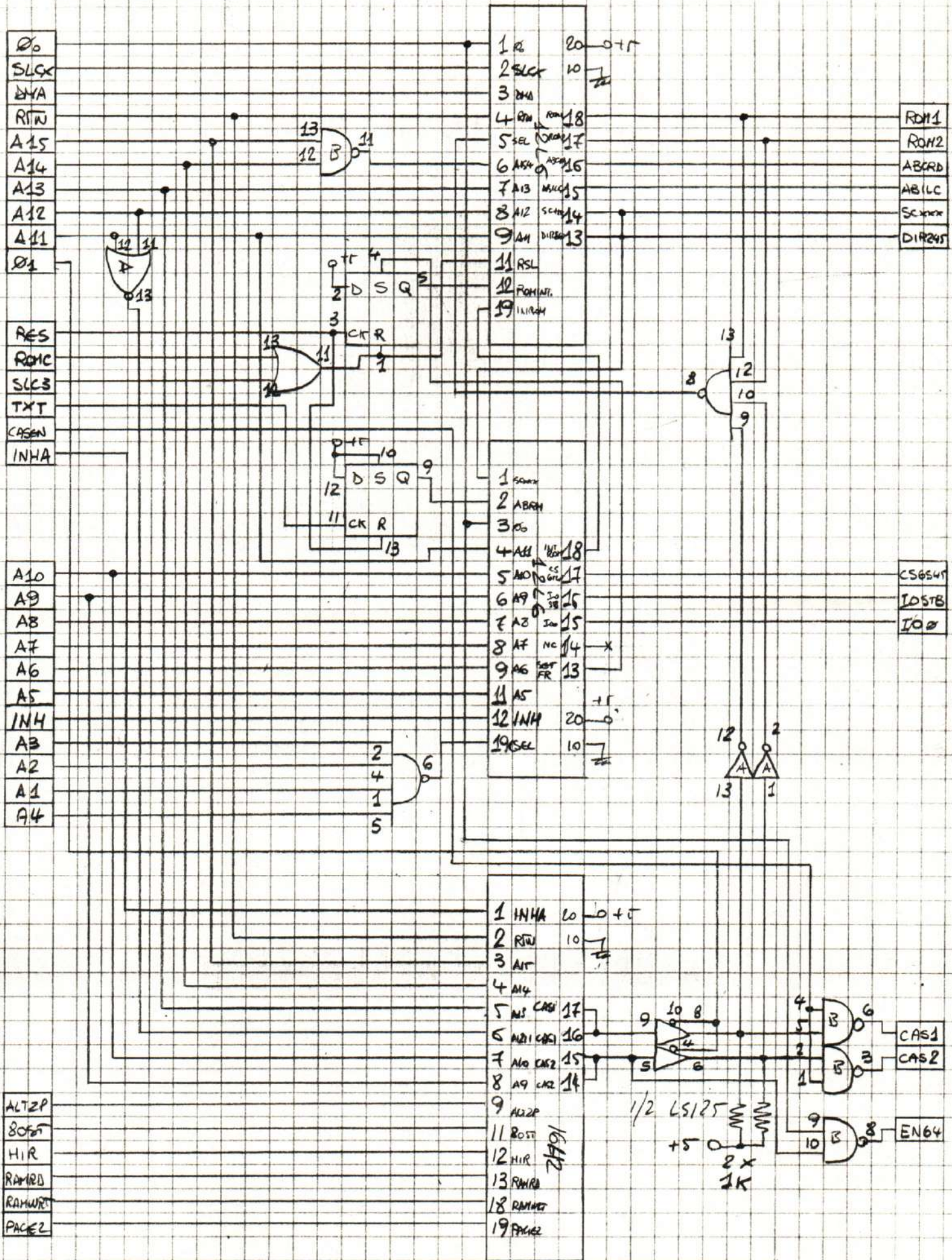


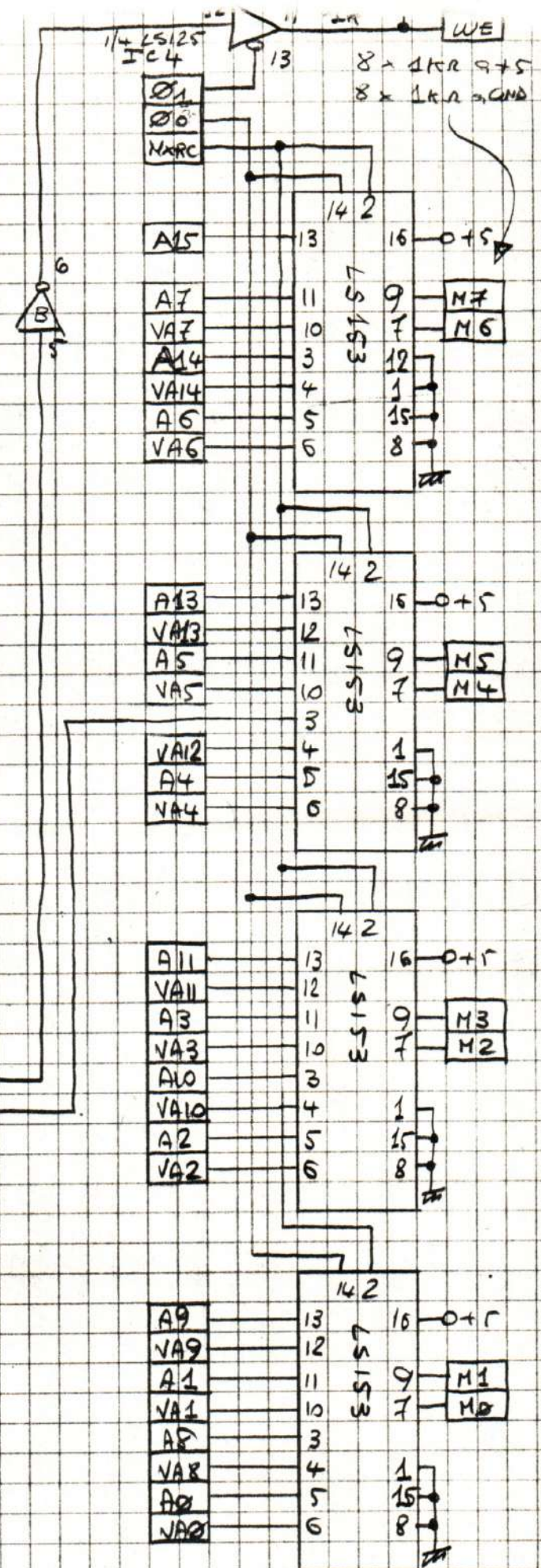
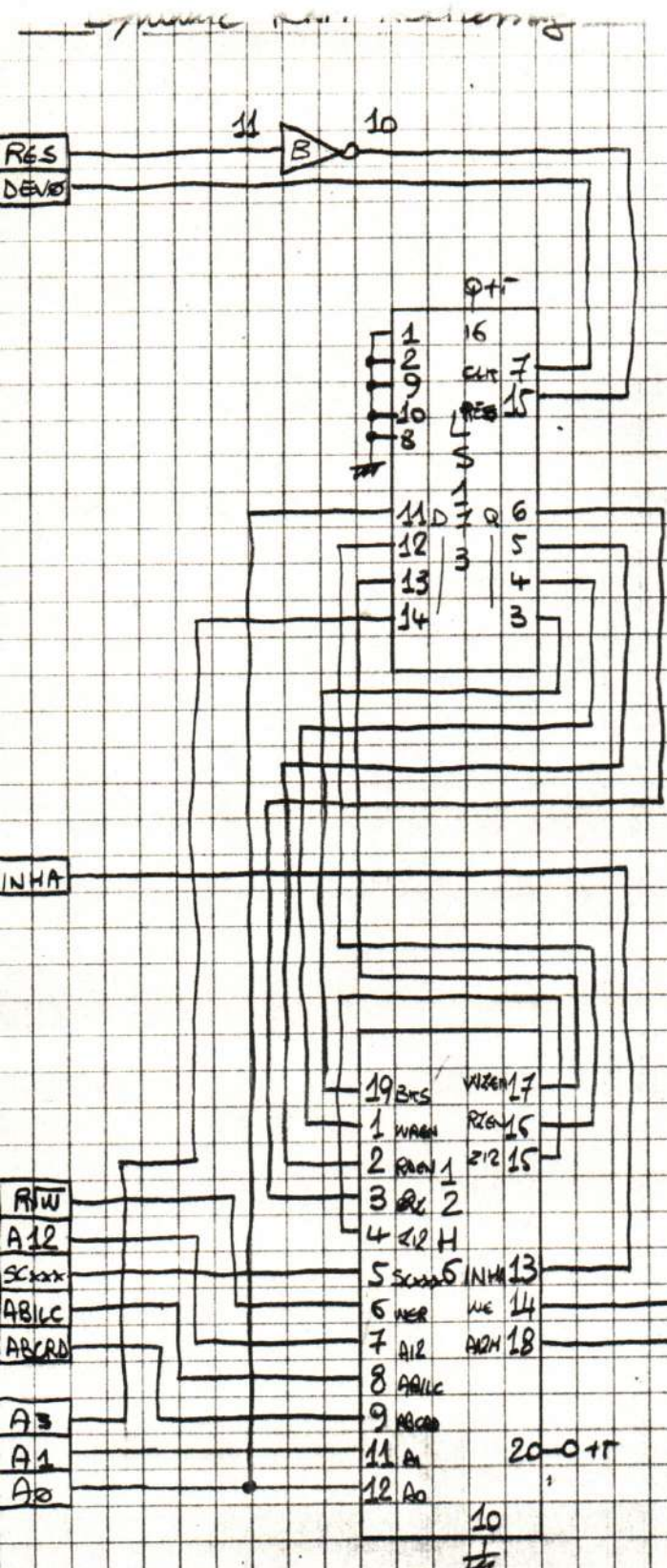
propagation delay 9 + 21 vpp

Spw + Loeffler

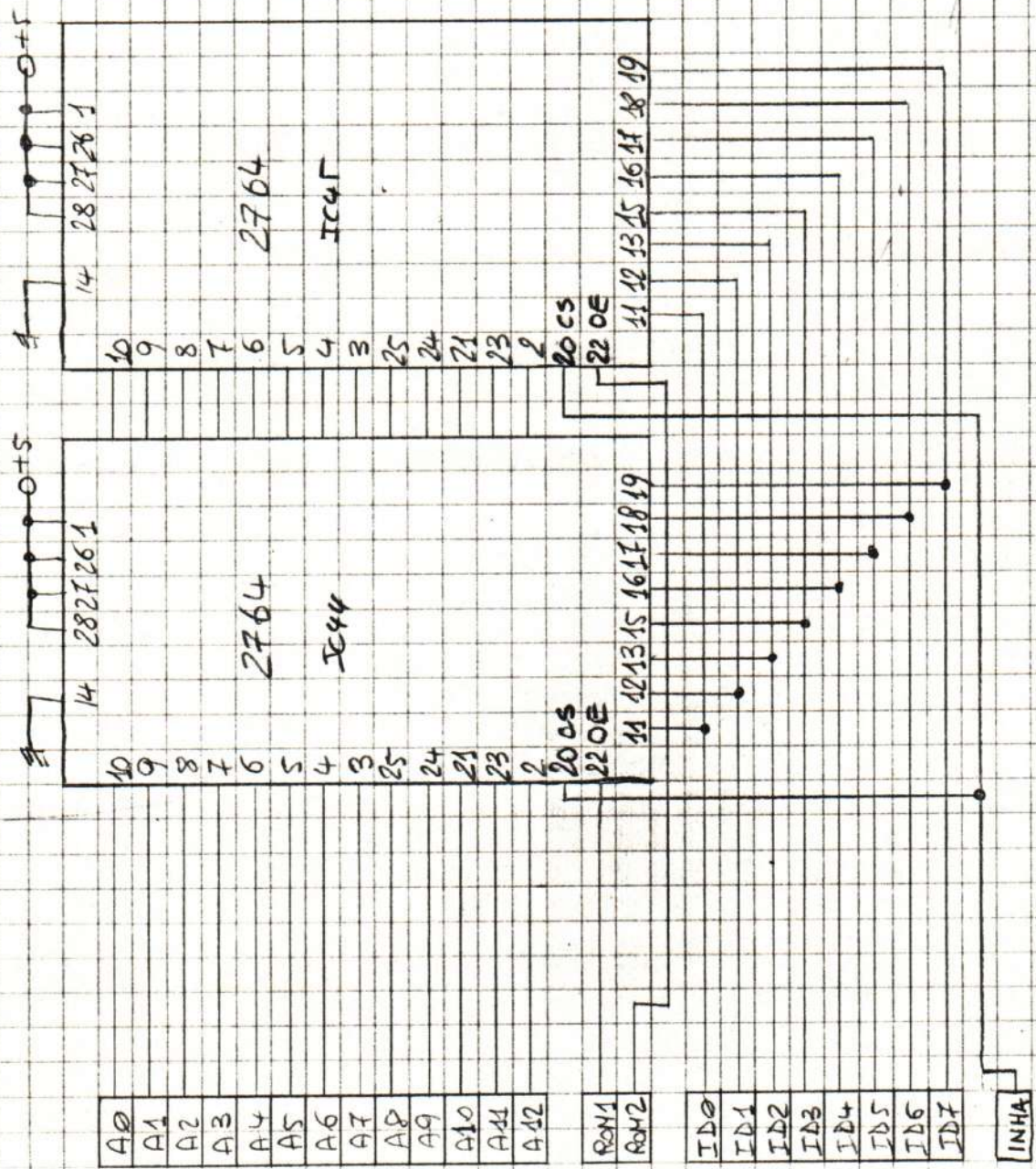


# Selezione

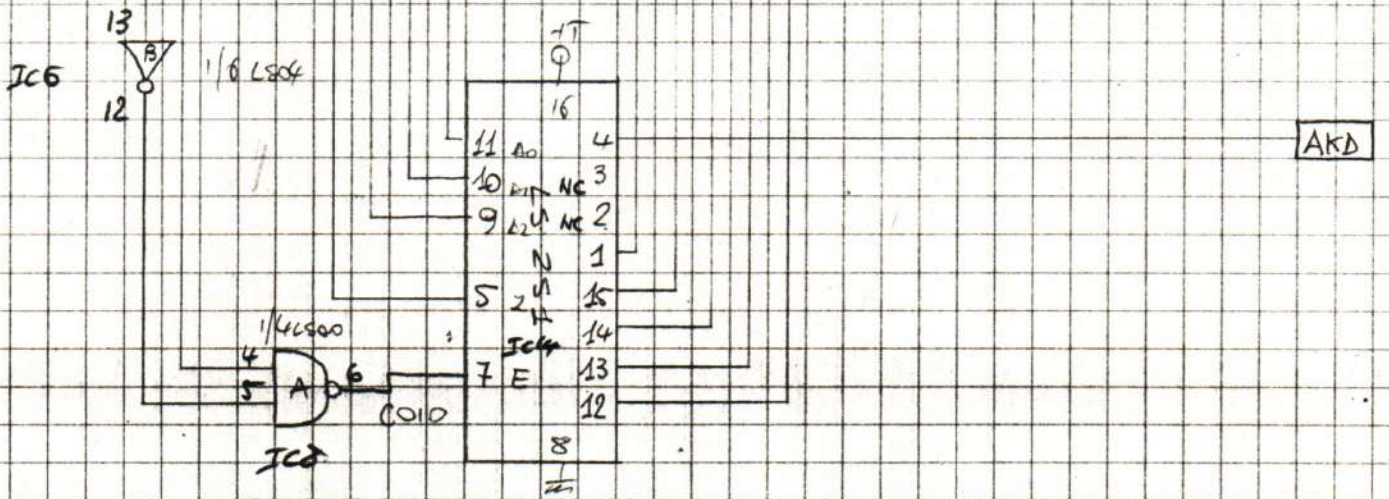
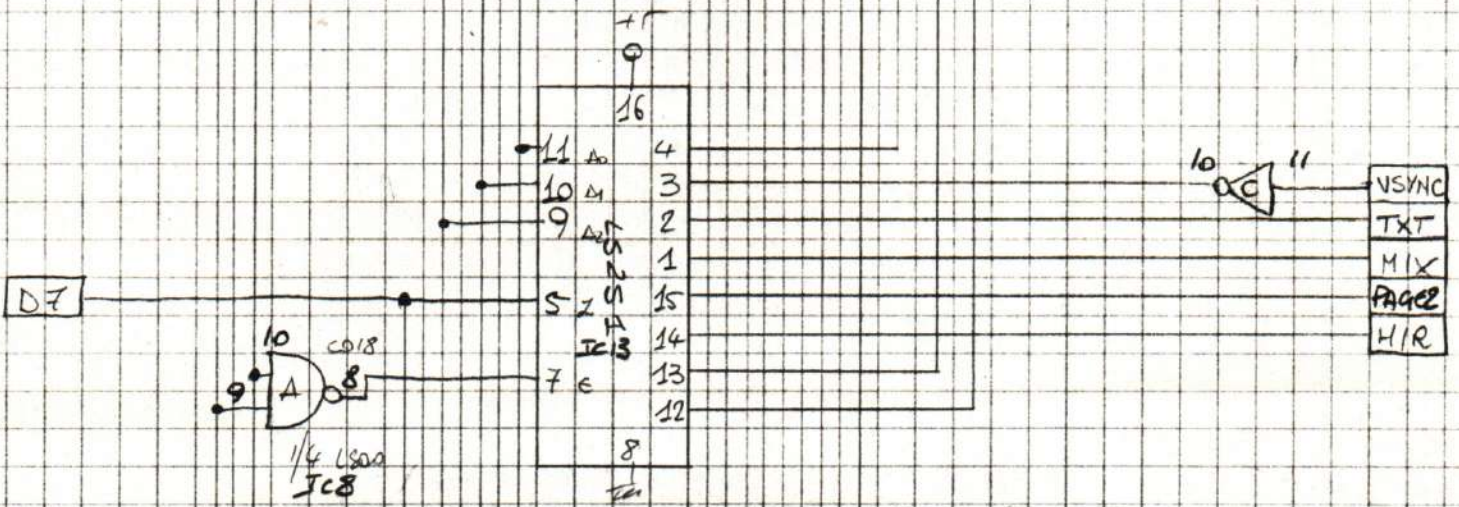
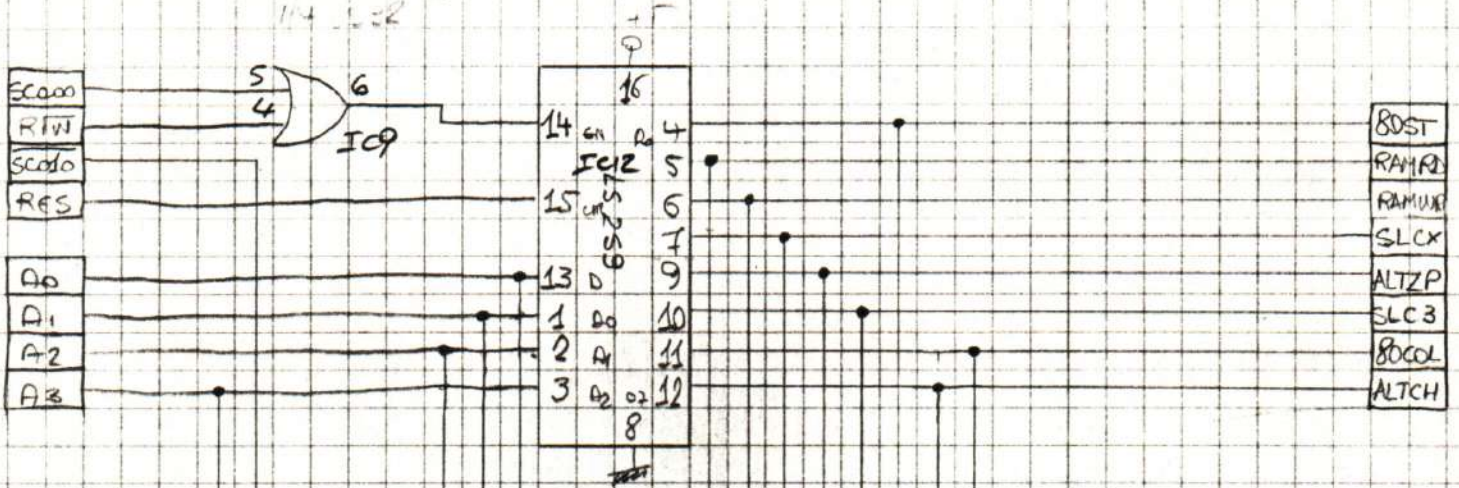




ROM 16K

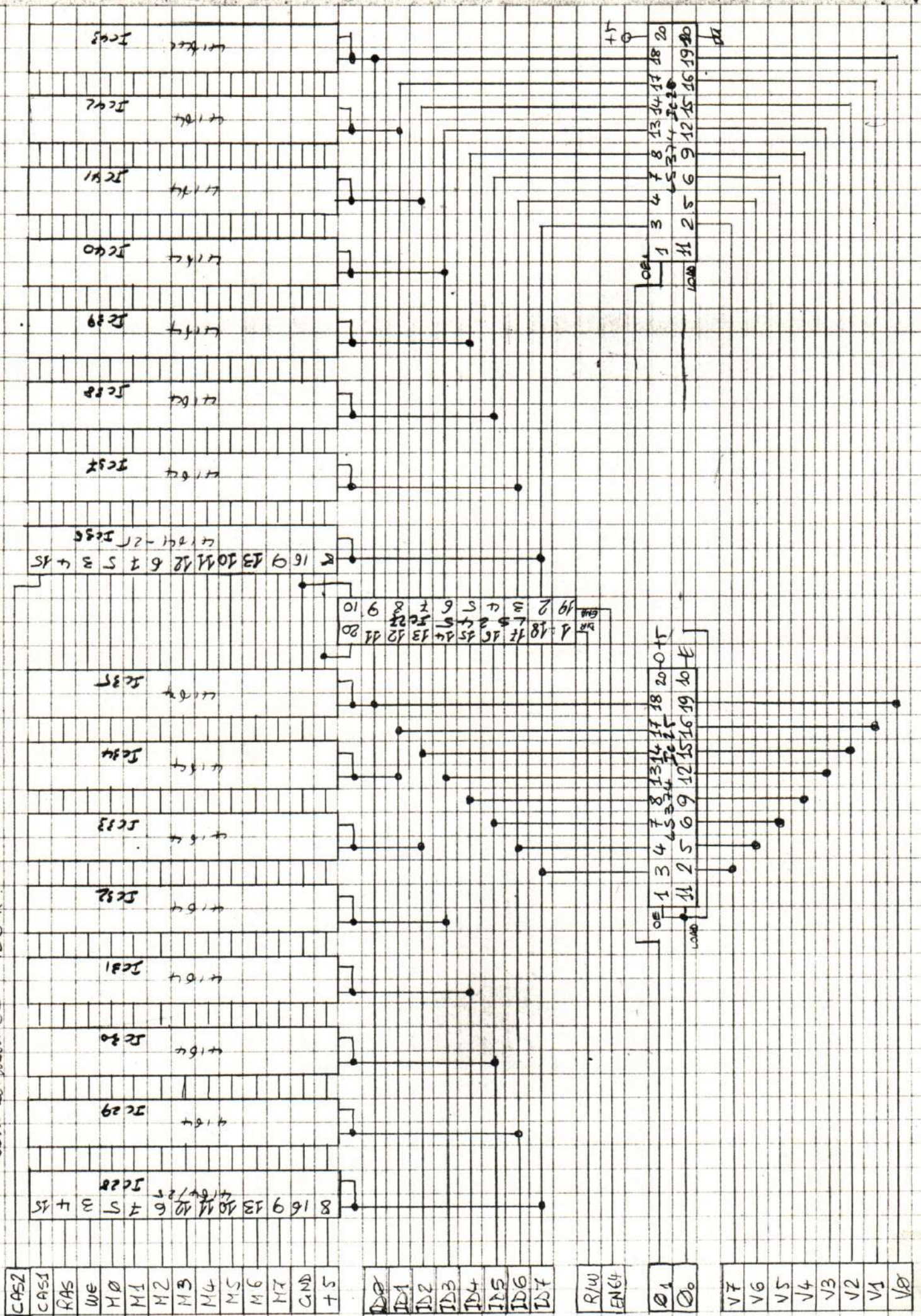


Soft reset  
 1/4 LS244





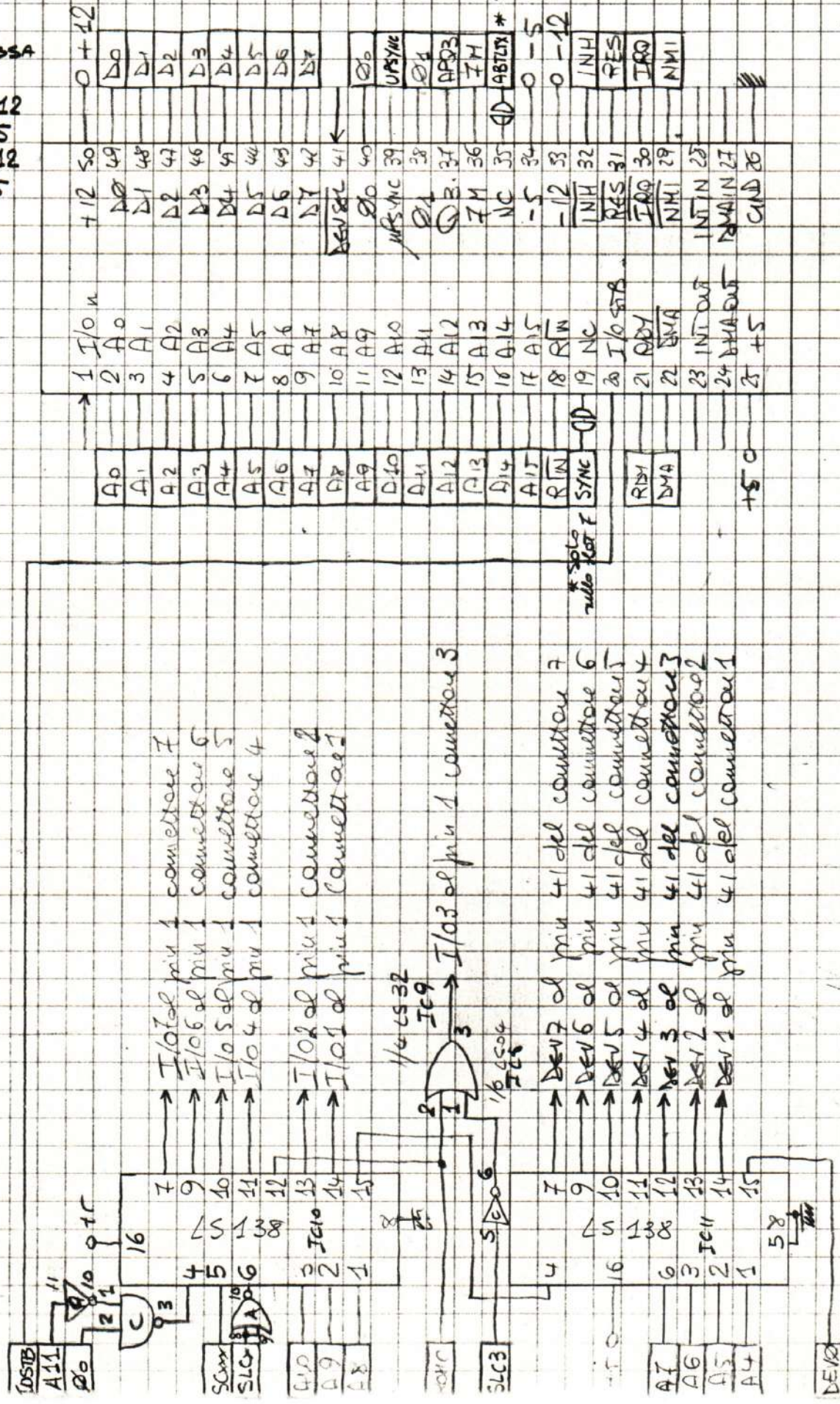
Row dinamica 128k



SLOTS (7)

Comettore di dimensionazione:

• • • •  
+ 12  
5  
+ 12  
5  
• MASSA



1	I/O	+12	50	0 + 12
2	A0	D0	49	D0
3	A1	D1	48	D1
4	A2	D2	47	D2
5	A3	D3	46	D3
6	A4	D4	45	D4
7	A5	D5	44	D5
8	A6	D6	43	D6
9	A7	D7	42	D7
10	A8	DEVI 41	←	
11	A9	D0	40	D0
12	A10	HVS NC	39	HVS NC
13	A11	D1	38	D1
14	A12	D2	37	D2
15	A13	FM	36	FM
16	A14	NC	35	NC
17	A15	-5	34	-5
18	R/W	-12	33	-12
19	NC	INH	32	INH
20	I/O STB	RES	31	RES
21	ADY	I/O	30	I/O
22	BVA	NMI	29	NMI
23	INTOUT	INTIN	25	INTIN
24	BVA OUT	MAININ	27	MAININ
25	+5	CND	26	CND

Comettore disponibile (1 di 7)

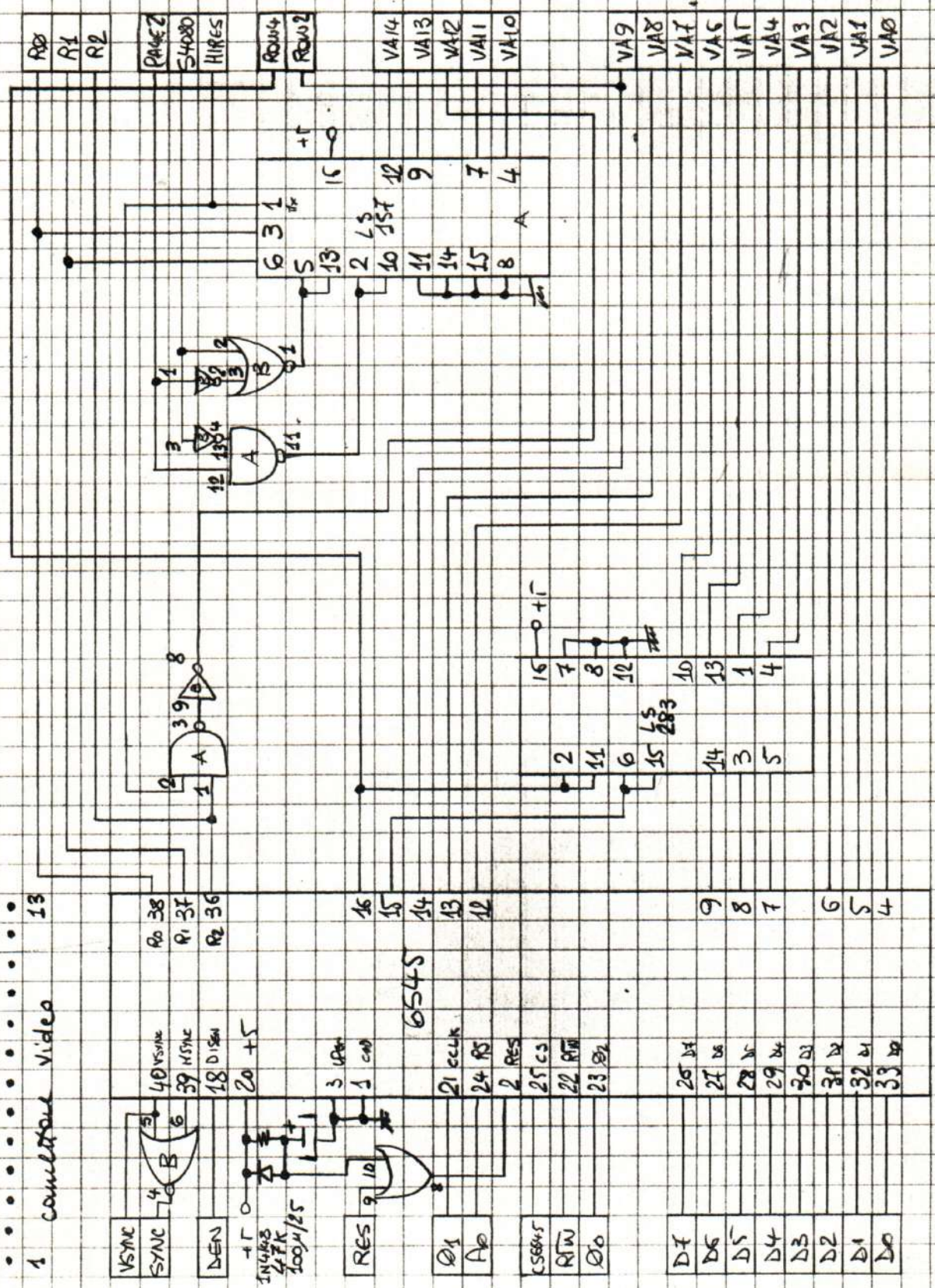
0 In SLOT 7, 19 → HVS NC, 35 ABTLTX

0 INTOUT e BVA OUT NOMIO connettore di INTIN e BVA  
- il connettore con numero pin è 0  
(1 di 2, 2 di 3, ecc)

# Video address generation

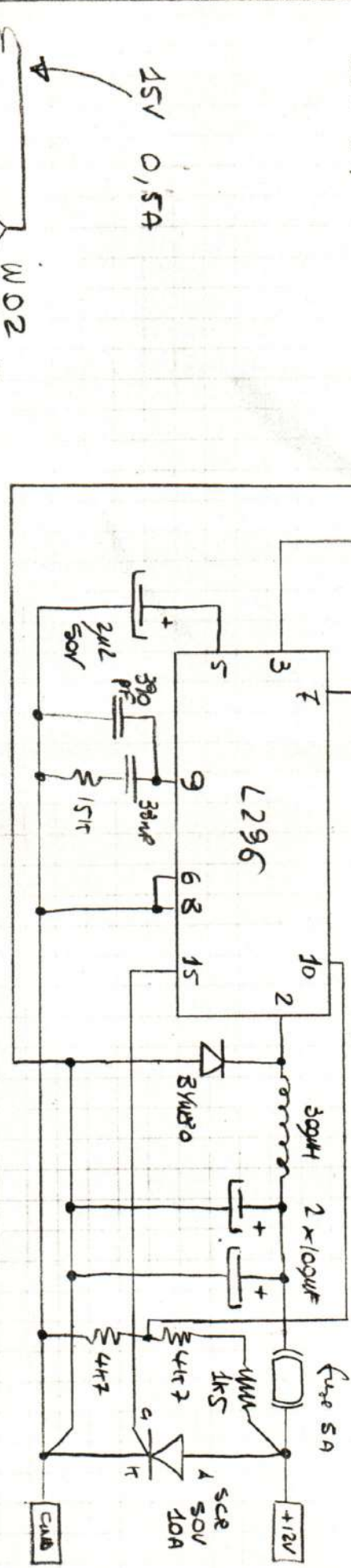
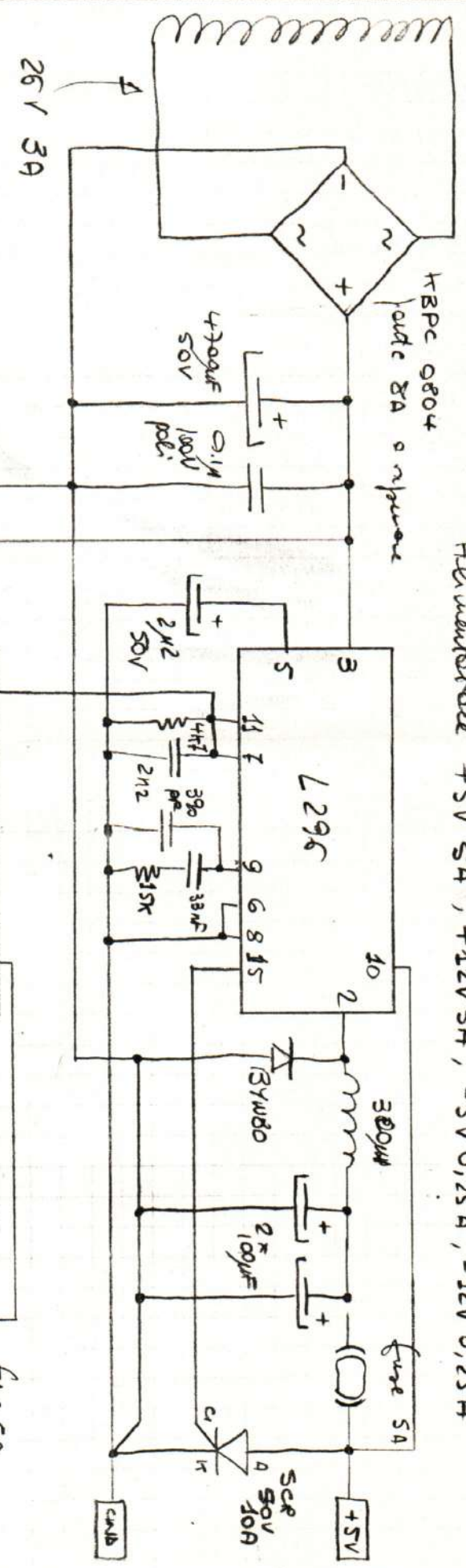
- CS
- +S
- TS7V1D
- +H2
- SYNC
- RH
- RL
- CH
- CL
- BX
- BX
- AB7TX
- AM3
- 13

1 complete video



VA9	VA8	VA7	VA6	VA5	VA4	VA3	VA2	VA1	VA0
25	26	27	28	29	30	31	32	33	34

Alimentación +5V SA, +12V SA, -5V 0,25A, -12V 0,25A



0V

Si se pone en serie sobre el terminal superior a 50V. la misma e'm punto comun.